Mitsubishi Ethernet Driver

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Mitsubishi Ethernet Driver

Help version 1.202

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Overview

The Mitsubishi Ethernet Driver provides a reliable way to connect Mitsubishi Ethernet devices to OPC client applications; including HMI, SCADA, Historian, MES, ERP, and countless custom applications. It is intended for use with Mitsubishi A Series and Mitsubishi Q Series devices communicating via the AJ71E71, A1SJ71E71, AJ71QE71, A1SJ71QE71, QJ71E71, or LJ71E71 Ethernet communications cards. A built-in Ethernet Port is supported for Q and L Series devices. This driver also supports the FX3U series PLC via the FX3U-ENET Ethernet module.

Note: Communications card model numbers listed are the base model number only. All suffixes are supported.

Setup

Supported Devices

A Series PLCs
QnA Series PLCs
Q (Q mode) Series PLCs
L Series PLCs
FX3U Series PLCs
iQ-R Series PLCs
iQ-F Series PLCs

Communication Protocol

Ethernet: using Winsock V1.1 or higher
TCP/IP, UDP

Supported Communication Parameters

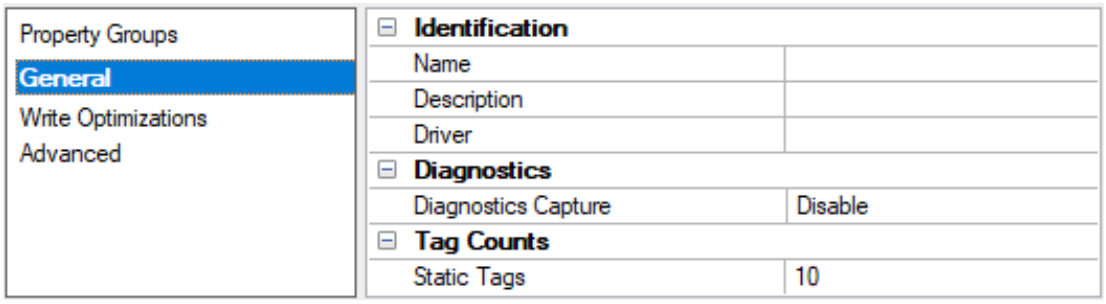
Binary format only

Channel and Device Limits

The maximum number of channels supported by this driver is 256. The maximum number of devices supported by this driver is 255 per channel.

Channel Properties — General

This server supports the use of multiple simultaneous communications drivers. Each protocol or driver used in a server project is called a channel. A server project may consist of many channels with the same communications driver or with unique communications drivers. A channel acts as the basic building block of an OPC link. This group is used to specify general channel properties, such as the identification attributes and operating mode.



Identification

Name: Specify the user-defined identity of this channel. In each server project, each channel name must be unique. Although names can be up to 256 characters, some client applications have a limited display window when browsing the OPC server's tag space. The channel name is part of the OPC browser information. The property is required for creating a channel.

For information on reserved characters, refer to "How To... Properly Name a Channel, Device, Tag, and Tag Group" in the server help.

Description: Specify user-defined information about this channel.

Many of these properties, including Description, have an associated system tag.

Driver: Specify the protocol / driver for this channel. This property specifies the device driver that was selected during channel creation. It is a disabled setting in the channel properties. The property is required for creating a channel.

Note: With the server's online full-time operation, these properties can be changed at any time. This includes changing the channel name to prevent clients from registering data with the server. If a client has already acquired an item from the server before the channel name is changed, the items are unaffected. If, after the channel name has been changed, the client application releases the item and attempts to re-acquire using the old channel name, the item is not accepted. Changes to the properties should not be made once a large client application has been developed. Utilize proper user role and privilege management to prevent operators from changing properties or accessing server features.

Diagnostics

Diagnostics Capture: When enabled, this option makes the channel's diagnostic information available to OPC applications allows the usage of statistics tags that provide feedback to client applications regarding the operation of the channel. Because the server's diagnostic features require a minimal amount of overhead processing, it is recommended that they be utilized when needed and disabled when not. The default is disabled.

Note: This property is not available if the driver does not support diagnostics.

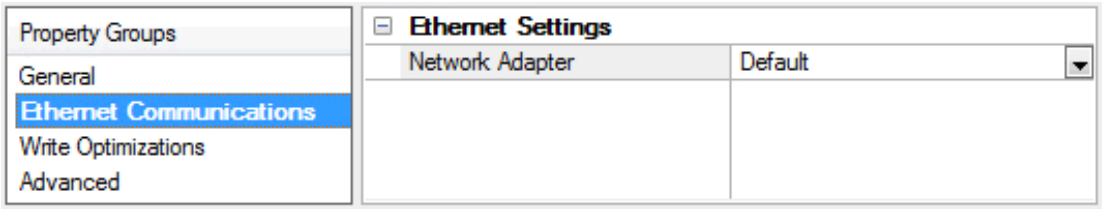
For more information, refer to "Communication Diagnostics" and "Statistics Tags" in the server help.

Tag Counts

Static Tags: Provides the total number of defined static tags at this level (device or channel). This information can be helpful in troubleshooting and load balancing.

Channel Properties — Ethernet Communications

Ethernet Communication can be used to communicate with devices.

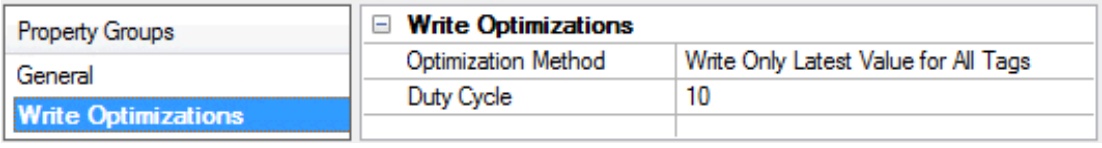


Ethernet Settings

Network Adapter: Specify the network adapter to bind. When left blank or Default is selected, the operating system selects the default adapter.

Channel Properties — Write Optimizations

The server must ensure that the data written from the client application gets to the device on time. Given this goal, the server provides optimization properties to meet specific needs or improve application responsiveness.



Write Optimizations

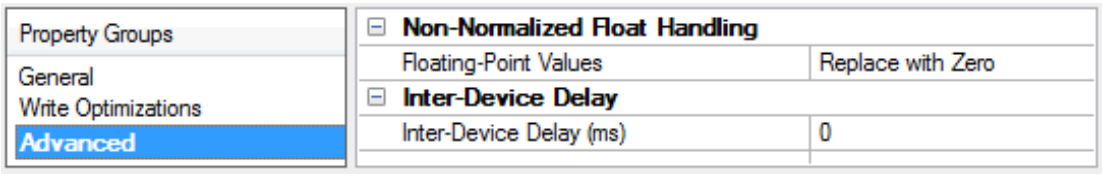
Optimization Method: Controls how write data is passed to the underlying communications driver. The options are:

- **Write All Values for All Tags**: This option forces the server to attempt to write every value to the controller. In this mode, the server continues to gather write requests and add them to the server's internal write queue. The server processes the write queue and attempts to empty it by writing data to the device as quickly as possible. This mode ensures that everything written from the client applications is sent to the target device. This mode should be selected if the write operation order or the write item's content must uniquely be seen at the target device.
- **Write Only Latest Value for Non-Boolean Tags**: Many consecutive writes to the same value can accumulate in the write queue due to the time required to actually send the data to the device. If the server updates a write value that has already been placed in the write queue, far fewer writes are needed to reach the same final output value. In this way, no extra writes accumulate in the server's queue. When the user stops moving the slide switch, the value in the device is at the correct value at virtually the same time. As the mode states, any value that is not a Boolean value is updated in the server's internal write queue and sent to the device at the next possible opportunity. This can greatly improve the application performance.
  Note: This option does not attempt to optimize writes to Boolean values. It allows users to optimize the operation of HMI data without causing problems with Boolean operations, such as a momentary push button.
- **Write Only Latest Value for All Tags**: This option takes the theory behind the second optimization mode and applies it to all tags. It is especially useful if the application only needs to send the latest value to the device. This mode optimizes all writes by updating the tags currently in the write queue before they are sent. This is the default mode.

Duty Cycle: is used to control the ratio of write to read operations. The ratio is always based on one read for every one to ten writes. The duty cycle is set to ten by default, meaning that ten writes occur for each read operation. Although the application is performing a large number of continuous writes, it must be ensured that read data is still given time to process. A setting of one results in one read operation for every write operation. If there are no write operations to perform, reads are processed continuously. This allows optimization for applications with continuous writes versus a more balanced back and forth data flow.
Note: It is recommended that the application be characterized for compatibility with the write optimization enhancements before being used in a production environment.

Channel Properties — Advanced

This group is used to specify advanced channel properties. Not all drivers support all properties; so the Advanced group does not appear for those devices.



Non-Normalized Float Handling: A non-normalized value is defined as Infinity, Not-a-Number (NaN), or as a Denormalized Number. The default is Replace with Zero. Drivers that have native float handling may default to Unmodified. Non-normalized float handling allows users to specify how a driver handles non-normalized IEEE-754 floating point data. Descriptions of the options are as follows:

- **Replace with Zero**: This option allows a driver to replace non-normalized IEEE-754 floating point values with zero before being transferred to clients.
- **Unmodified**: This option allows a driver to transfer IEEE-754 denormalized, normalized, non-number, and infinity values to clients without any conversion or changes.

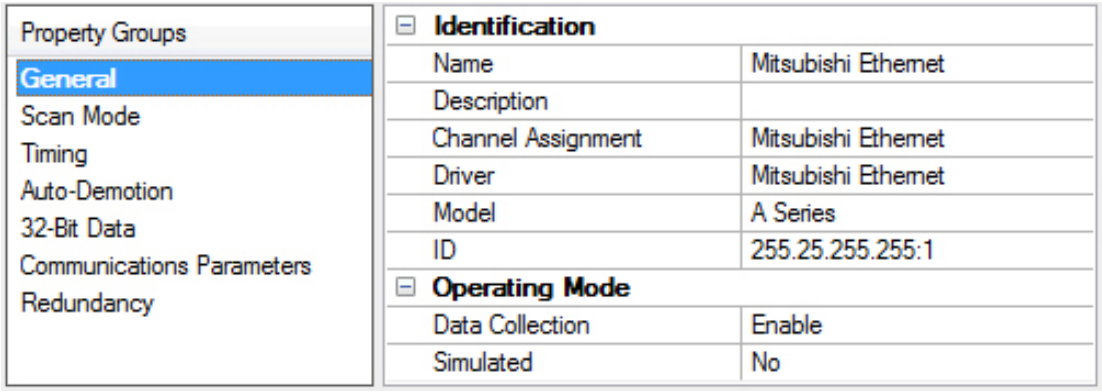
Note: This property is disabled if the driver does not support floating-point values or if it only supports the option that is displayed. According to the channel's float normalization setting, only real-time driver tags (such as values and arrays) are subject to float normalization. For example, EFM data is not affected by this setting.

For more information on the floating-point values, refer to "How To ... Work with Non-Normalized Floating-Point Values" in the server help.

Inter-Device Delay: Specify the amount of time the communications channel waits to send new requests to the next device after data is received from the current device on the same channel. Zero (0) disables the delay.

Note: This property is not available for all drivers, models, and dependent settings.

Device Properties — General



Identification

Name: User-defined identity of this device.

Description: User-defined information about this device.

Channel Assignment: User-defined name of the channel to which this device currently belongs.

Driver: Selected protocol driver for this device.

Model: The specific version of the device. Options include A Series (for all A Series PLCs), L Series (for all L Series PLCs), Q Series (for all Q Series PLCs), QnA Series (for all QnA Series PLCs), and FX3U (for all FX3U Series PLCs).

ID (PLC Network Address): the unique device identity used to specify the IP address with a PC number (and net number if the device is a Q series PLC).

- **A Series:** Device IDs are specified as YYY.YYY.YYY.YYY:XXX. The YYY designates the device IP address (each YYY byte should be in the range of 0 to 255). The XXX designates the PC Number of the target device and can be in the range of 0 to 64 or 255 for the local PC.
- **Q Series:** Device IDs are specified as YYY.YYY.YYY.YYY:Nzzz:XXX or YYY.YYY.YYY.YYY:nzzz:XXX. The YYY designates the device IP address (each YYY byte should be in the range of 0 to 255). The zzz designates the Network Number of the target device and can be in the range of 0 to 255. The XXX designates the PC Number of the target device and can be in the range of 0 to 64 or 255 for the local PC.
  Note: For a local connection, which is network 0, the network number can be omitted, resulting in the format YYY.YYY.YYY.YYY:XXX. *For more information, refer to* ***Multi-level Networks***.
- **QnA Series**: Device IDs are specified as YYY.YYY.YYY.YYY:Nzzz:XXX or YYY.YYY.YYY.YYY:nzzz:XXX. The YYY designates the device IP address (each YYY byte should be in the range of 0 to 255). The zzz designates the Network Number of the target device and can be in the range of 0 to 255. The XXX designates the PC Number of the target device and can be in the range of 0 to 64 or 255 for the local PC.
- **L Series**: Device IDs are specified as YYY.YYY.YYY.YYY:Nzzz:XXX or YYY.YYY.YYY.YYY:nzzz:XXX. The YYY designates the device IP address (each YYY byte should be in the range of 0 to 255). The zzz designates the Network Number of the target device and can be in the range of 0 to 255. The XXX designates the PC Number of the target device and can be in the range of 0 to 64 or 255 for the local PC.
- **FX3U:** Device IDs are specified as YYY.YYY.YYY.YYY:XXX. The YYY designates the device IP address (each YYY byte should be in the range of 0 to 255). The XXX designates the PC Number of the target device and can be in the range of 0 to 15 or 255 for the local PC.

Note: The AJ71E71, A1SJ71E71, AJ71QE71, A1SJ71QE71, LJ71E71, and QJ71E71 families of communications cards occupy ranges of X and Y memory. Writing to this memory may disable the card and cause loss of communications. *For more information, refer to the communications card manual.*

Operating Mode

Data Collection: This property controls the device's active state. Although device communications are enabled by default, this property can be used to disable a physical device. Communications are not attempted when a device is disabled. From a client standpoint, the data is marked as invalid and write operations are not accepted. This property can be changed at any time through this property or the device system tags.

Simulated: This option places the device into Simulation Mode. In this mode, the driver does not attempt to communicate with the physical device, but the server continues to return valid OPC data. Simulated stops physical communications with the device, but allows OPC data to be returned to the OPC client as valid data. While in Simulation Mode, the server treats all device data as reflective: whatever is written to the simulated device is read back and each OPC item is treated individually. The item's memory map is based on the group Update Rate. The data is not saved if the server removes the item (such as when the server is reinitialized). The default is No.

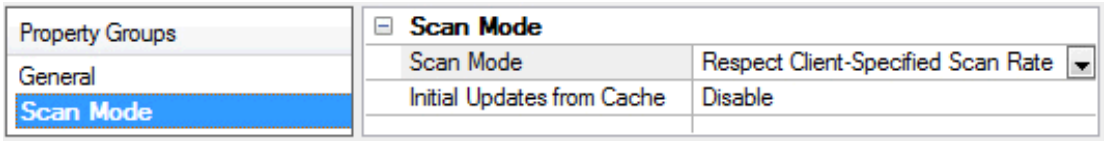
Notes:

1. This System tag (_Simulated) is read only and cannot be written to for runtime protection. The System tag allows this property to be monitored from the client.
2. In Simulation mode, the item's memory map is based on client update rate(s) (Group Update Rate for OPC clients or Scan Rate for native and DDE interfaces). This means that two clients that reference the same item with different update rates return different data.

Simulation Mode is for test and simulation purposes only. It should never be used in a production environment.

Device Properties — Scan Mode

The Scan Mode specifies the subscribed-client requested scan rate for tags that require device communications. Synchronous and asynchronous device reads and writes are processed as soon as possible; unaffected by the Scan Mode properties.



Scan Mode: Specify how tags in the device are scanned for updates sent to subscribing clients. Descriptions of the options are:

- **Respect Client-Specified Scan Rate**: This mode uses the scan rate requested by the client.
- **Request Data No Faster than Scan Rate**: This mode specifies the value set as the maximum scan rate. The valid range is 10 to 99999990 milliseconds. The default is 1000 milliseconds.
  Note: When the server has an active client and items for the device and the scan rate value is increased, the changes take effect immediately. When the scan rate value is decreased, the changes do not take effect until all client applications have been disconnected.
- **Request All Data at Scan Rate**: This mode forces tags to be scanned at the specified rate for subscribed clients. The valid range is 10 to 99999990 milliseconds. The default is 1000 milliseconds.
- **Do Not Scan, Demand Poll Only**: This mode does not periodically poll tags that belong to the device nor perform a read to get an item's initial value once it becomes active. It is the OPC client's responsibility to poll for updates, either by writing to the _DemandPoll tag or by issuing explicit device reads for individual items. *For more information, refer to "Device Demand Poll" in server help*.
- **Respect Tag-Specified Scan Rate**: This mode forces static tags to be scanned at the rate specified in their static configuration tag properties. Dynamic tags are scanned at the client-specified scan rate.

Initial Updates from Cache: When enabled, this option allows the server to provide the first updates for newly activated tag references from stored (cached) data. Cache updates can only be provided when the new item reference shares the same address, scan rate, data type, client access, and scaling properties. A device read is used for the initial update for the first client reference only. The default is disabled; any time a client activates a tag reference the server attempts to read the initial value from the device.

Device Properties — Timing

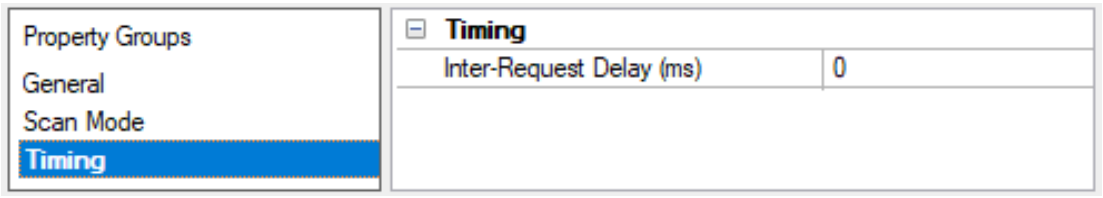
The device Timing properties allow the driver's response to error conditions to be tailored to fit the application's needs. In many cases, the environment requires changes to these properties for optimum performance. Factors such as electrically generated noise, modem delays, and poor physical connections can influence how many errors or timeouts a communications driver encounters. Timing properties are specific to each configured device.

Timing

Inter-Request Delay: Specify how long the driver waits before sending the next request to the target device. It overrides the normal polling frequency of tags associated with the device, as well as one-time reads and writes. This delay can be useful when dealing with devices with slow turnaround times and in cases where network load is a concern. Configuring a delay for a device affects communications with all other devices on the channel. It is recommended that users separate any device that requires an inter-request delay to a separate channel if possible. Other communications properties (such as communication serialization) can extend this delay. The valid range is 0 to 300,000 milliseconds; however, some drivers may

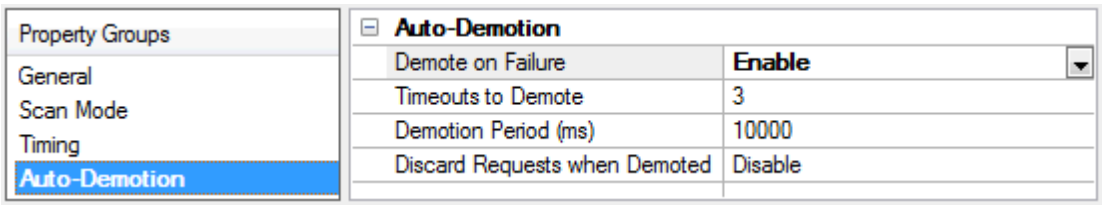
limit the maximum value due to a function of their particular design. The default is 0, which indicates no delay between requests with the target device.

Note: Not all drivers support Inter-Request Delay. This setting does not appear if it is not available.



Device Properties — Auto-Demotion

The Auto-Demotion properties can temporarily place a device off-scan in the event that a device is not responding. By placing a non-responsive device offline for a specific time period, the driver can continue to optimize its communications with other devices on the same channel. After the time period has been reached, the driver re-attempts to communicate with the non-responsive device. If the device is responsive, the device is placed on-scan; otherwise, it restarts its off-scan time period.



Demote on Failure: When enabled, the device is automatically taken off-scan until it is responding again.

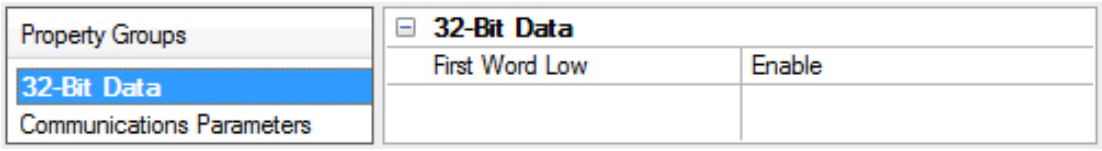
Tip: Determine when a device is off-scan by monitoring its demoted state using the _AutoDemoted system tag.

Timeouts to Demote: Specify how many successive cycles of request timeouts and retries occur before the device is placed off-scan. The valid range is 1 to 30 successive failures. The default is 3.

Demotion Period: Indicate how long the device should be placed off-scan when the timeouts value is reached. During this period, no read requests are sent to the device and all data associated with the read requests are set to bad quality. When this period expires, the driver places the device on-scan and allows for another attempt at communications. The valid range is 100 to 3600000 milliseconds. The default is 10000 milliseconds.

Discard Requests when Demoted: Select whether or not write requests should be attempted during the off-scan period. Disable to always send write requests regardless of the demotion period. Enable to discard writes; the server automatically fails any write request received from a client and does not post a message to the Event Log.

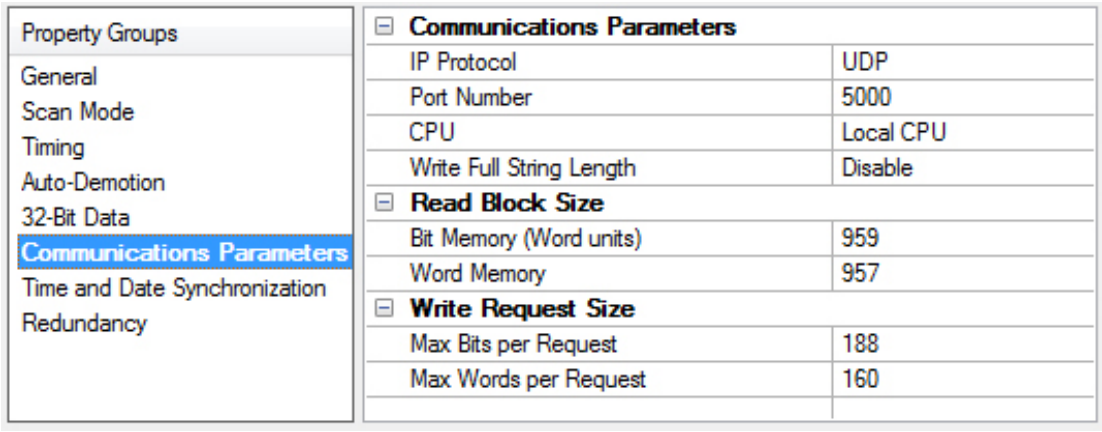
Device Properties — 32-Bit Data



First Word Low: In a Mitsubishi Ethernet device, the addresses of two consecutive registers are used for 32-bit data types. When this option is enabled, the driver assumes the first word is low for the 32-bit value. When this option is disabled, the driver assumes that the first word is high for the 32-bit value. The default setting is enabled.

Note: This property can't be changed while there are active references on the device.

Device Properties — Communications Parameters



Communications Parameters

IP Protocol: Select the IP protocol: TCP/IP or UDP. TCP/IP is less efficient than UDP and requires a special ladder for network error recovery in the A and QnA series PLCs. Furthermore, Q series users planning to communicate with devices on a remote network must configure multiple ports in the relay device when using TCP/IP. As such, UDP is recommended wherever possible.

For more information, refer to ***Multi-level Networks****.*

Port Number: Specify the port number to use when communicating with the device. The default for UDP is 5000. The default for TCP is 5001.

CPU: Specify the target CPU to connect with. For a single CPU, select Local CPU. The default value is Local CPU.

Notes:

1. The default settings are based on GX Developer version 8.25B.
2. This property can't be changed while there are active references to the device.

Communications Parameters	
IP Protocol	UDP
Port Number	5000
Source Port Number	0
Write Full String Length	Disable

Source Port Number: For UDP protocol, model FX3U only, specify the port number the device expects the driver to send from when communicating with the device. The default of 0 means the system picks the source port.

Tip: Use this property for model FX3U devices configured for and expecting a Destination Port Number.

Write Full String Length: Specify if the driver should add NULL character bytes after the end of a string when writing to the device. Adding NULL character bytes after the end of the string ensures that the device memory for the string only contains the characters that were written. The default setting is **False**.

Read Block Size

Bit Memory: Set the maximum number of words per read request of bit-based memory. The default value is the maximum value allowed.

	Minimum Bit Block Size	Maximum Bit Block Size
A Series	One Word	127 Words
Q Series	One Word	959 Words
QnA Series	One Word	479 Words
L Series	One Word	959 Words
FX3U Series	One Word	31 Words
iQ-R Series	One Word	959 Words
iQ-F Series	One Word	959 Words

Word Memory: Set the maximum number of words per read request of word-based memory. The default value is the maximum value allowed.

	Minimum Word Block Size	Maximum Word Block Size
A Series	One Word	253 Words
Q Series	One Word	957 Words
QnA Series	One Word	477 Words
L Series	One Word	957 Words
FX3U Series	One Word	61 Words
iQ-R Series	One Word	957 Words
iQ-F Series	One Word	957 Words

Note: If opening an .opf or .xml file from version 6.0 or earlier, the Bit/Word Block Size is initially set to values shown in the following table to match earlier versions. If opening a JSON file saved with 6.0, the properties are set to the default values.

	Initial Bit Block Size	Initial Word Block Size
A Series	125 Words	252 Words
Q Series	125 Words	252 Words
FX3U Series	30 Words	60 Words

Write Request Size

Max Bits per Request: Set the maximum number of bit tags to be processed per write request. If writing to a size larger than this setting, multiple write requests are processed. The default value is the maximum value allowed.

	Minimum Bits per Request	Maximum Bits per Request
Q Series	One Bit	188 Bits
QnA Series	One Bit	94 Bits
L Series	One Bit	188 Bits
iQ-R Series	One Bit	94 Bits
iQ-F Series	One Bit	188 Bits

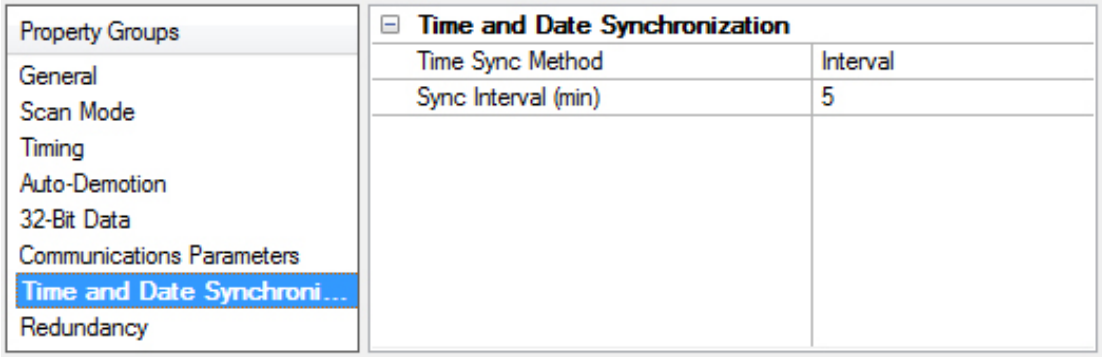
Max Words per Request: Set the maximum number of words to write per request. If writing to a size larger than this setting, multiple write requests are processed. The default value is the maximum value allowed.

	Minimum Words per Request	Maximum Words per Request
Q Series	One Word	160 Words
QnA Series	One Word	80 Words
L Series	One Word	160 Words
iQ-R Series	One Word	80 Words
iQ-F Series	One Word	160 Words

Note: If opening an .opf or .xml file from version 6.0 or earlier, Max Bits per Request and Max Words per Request are initially set to 1. If opening a JSON file saved with 6.0, the properties are set to the default values.

Device Properties — Time and Date Synchronization

Time and Date Synchronization properties are only available to the Q, QnA, and L Series Model PLCs.



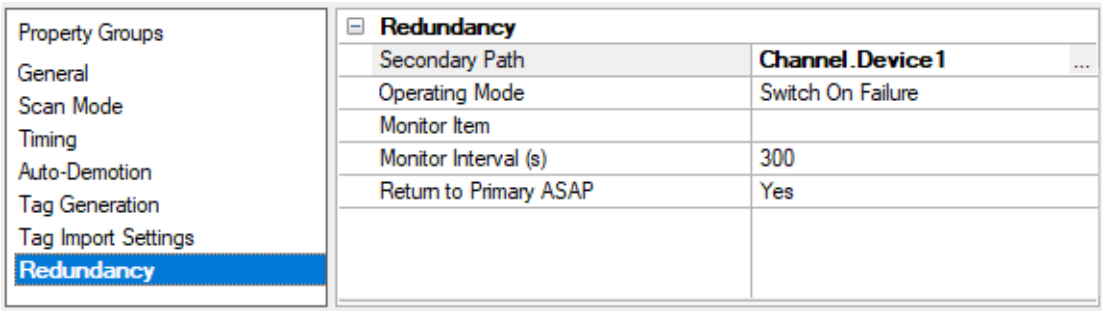
Time Sync Method: Choose the synchronization method to define how the time and date are reconciled between the host system and the device. Options include Disabled, Interval, and Absolute. The default setting is Disabled.

Absolute Sync Time: Specify an exact hour and minute of each day to synchronize time between the server and the device when the synchronization method is Absolute. The default value is set to the local PC time when the device was created. Only the hour and minute of the day are used to determine if synchronization is required. The seconds are ignored. As an example, if this property displays as 3:52:00 PM, time synchronization will occur each day at 57120 seconds after midnight.

Sync Interval: Specify the time, in minutes, between synchronizations - how often time and date reconciliation should occur when the synchronization method is Interval. The driver can periodically synchronize the time and date of the PLC with the time and date of the host computer. The valid range is 5 to 1440 minutes (24 hours). The default setting is 5 minutes.

Note: For example, if 240 minutes is entered, the driver sets the PLC date and time every 4 hours.

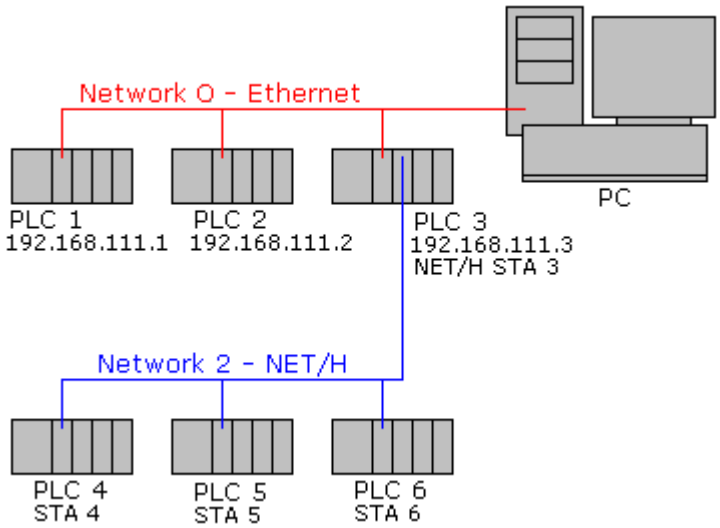
Device Properties — Redundancy



Redundancy is available with the Media-Level Redundancy Plug-In.
Consult the website, a sales representative, or the user manual for more information.

Multi-Level Networks

The Q Series, QnA Series, and L Series models are used to communicate with devices on remote networks. In the example shown below, PLC 1, PLC 2 and PLC 3 are on the local Ethernet network (Network 0). PLC 4, PLC 5 and PLC 6 are on a remote NET/H network. PLC 3 serves as a relay device connecting the two networks.



If PLC 1, PLC 2 and PLC 3 have QJ71E71-100 Ethernet modules configured with IPs 192.168.111.1, 192.168.111.2 and 192.168.111.3 respectively. In addition to the Ethernet module, PLC 3 also has a QJ71BR11 NET/H module configured as station 3. Assume that PLC 4, PLC 5 and PLC 6 have NET/H modules configured as stations 4, 5 and 6 respectively.

To communicate with all six PLCs, six devices would need to be created in the server project. The Device IDs would be as follows:

PLC	Device ID	Comment
1	192.168.111.1:N0:255*	Local network, local PC
2	192.168.111.2:N0:255*	Local network, local PC
3	192.168.111.3:N0:255*	Local network, local PC
4	192.168.111.3:N2:4	Network 2, PC 4, via PLC 3
5	192.168.111.3:N2:5	Network 2, PC 5, via PLC 3
6	192.168.111.3:N2:6	Network 2, PC 6, via PLC 3

* This example shows: N0 as the network number for the local network. It is also possible to omit the network number when it is Network 0 (local network), thus, the Device ID 192.168.111.1:255 would also be valid in this case.

Notes:

1. For performance and reliability, the driver is designed to use a separate socket for each device. Thus, if TCP/IP is used, the relay device in this example would need to have at least 4 ports configured - one to connect to each of the driver's sockets for PLC 3, PLC 4, PLC 5 and PLC 6. However, only a single port needs to be configured in the relay device if UDP and the "unspecified" destination IP

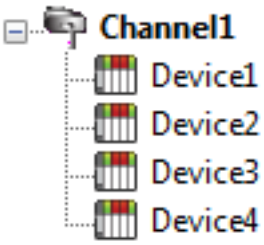
(255.255.255.255) and port number (0xFFFF) are being used. Therefore, UDP is generally recommended for this type of application. For more information, refer to **PLC Setup**.

2. A relay device may take 5 or more seconds to report a failed read and write to a remote device. It is recommended that the request timeout for remote devices be set accordingly. *For more information, refer to **Device Setup**.*

Optimizing Communications

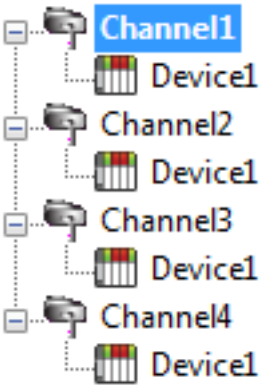
The Mitsubishi Ethernet Driver has been designed to provide the best performance with the least amount of impact on the system's overall performance. While the Mitsubishi Ethernet Driver is fast, there are a couple of guidelines that can be used to control and optimize the application and gain maximum performance.

This server refers to communications protocols like Mitsubishi Ethernet Device as a channel. Each channel defined in the application represents a separate path of execution in the server. Once a channel has been defined, a series of devices must then be defined under that channel. Each of these devices represents a single Mitsubishi Ethernet device from which data should be collected. While this approach to defining the application provides a high level of performance, it doesn't take full advantage of the Mitsubishi Ethernet Driver or the network. An example of how the application may appear when configured using a single channel is shown below.



Each device appears under a single Mitsubishi Ethernet device channel. In this configuration, the driver must move from one device to the next as quickly as possible to gather information at an effective rate. As more devices are added or more information is requested from a single device, the overall update rate begins to suffer.

If the Mitsubishi Ethernet Driver could only define one single channel, then the example shown above would be the only option available; however, the driver can define up to 256 channels. Using multiple channels distributes the data collection workload by simultaneously issuing multiple requests to the network. An example of how the same application may appear when configured using multiple channels to improve performance is shown below.



Each device can be defined under its own channel. In this configuration, a single path of execution is dedicated to the task of gathering data from each device. If the application has 256 or fewer devices, it can be optimized exactly how it is shown here.

The performance improves even if the application has more devices. While fewer devices may be ideal, the application still benefits from additional channels. Although by spreading the device load across all channels causes the server to move from device to device again, it can do so with far less devices to process on a single channel.

Tip: An additional performance gain can be achieved by using UDP instead of TCP/IP. *For more information, refer to* ***Device Setup*** *and* ***PLC Setup****.*

Data Types Description

The Mitsubishi Ethernet Driver supports the following data types.

Data Type	Description
Boolean	Single bit
Word	Unsigned 16-bit value bit 0 is the low bit bit 15 is the high bit
Short	Signed 16-bit value bit 0 is the low bit bit 14 is the high bit bit 15 is the sign bit
DWord	Unsigned 32-bit value Bit 0 is the low bit. Bit 31 is the high bit.
Long	Signed 32-bit value Bit 0 is the low bit. Bit 30 is the high bit. Bit 31 is the sign bit.
Float	32-bit floating point value
String	Null-terminated ASCII string support includes HiLo and LoHi byte order selection and string lengths up to 128 bytes.
BCD	Two byte packed BCD Value range is 0-9999. Behavior is undefined for values beyond this range.
LBCD	Four byte packed BCD Value range is 0-99999999. Behavior is undefined for values beyond this range.
Date	32-bit value
Date Example	Date format: YYYY-MM-DDTHH:MM:SS.000 2000-01-01T12:30:45.000
Double*	64-bit floating point value The driver interprets four consecutive registers as a Double precision value by making the first two registers the low DWord and the last two registers the high DWord.
Double Example*	If register D0000000 is specified as a Double, bit 0 of register D0000000 would be bit 0 of the 64-bit data type. Bit 15 of register D0000003 would be bit 63 of the 64-bit data type.

* The descriptions above assume the default first word low data handling of 32-bit data types.

Address Descriptions

Address specifications vary depending on the model in use. Select a link from the following list to obtain specific address information for the model of interest.

Mitsubishi A Series Address Descriptions

The default data types for dynamically defined tags are shown in **bold**.

Device Type	Range	Data Type	Access
Inputs*	X000-X1FFF (Hex) X000-X1FF0 (Hex) X000-X1FE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Outputs*	Y000-Y1FFF (Hex) Y000-Y1FF0 (Hex) Y000-Y1FE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Link Relays*	B000-B1FFF (Hex) B000-B1FF0 (Hex) B000-B1FE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Internal Relays*	M0000-M8191 M0000-M8176 M0000-M8160	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Special Int. Relays*	M9000-M9255 M9000-M9240 M9000-M9224	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read Only
Latch Relays*	L0000-L8191 L0000-L8176 L0000-L8160	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Annunciator Relays*	F0000-F2047 F0000-F2032 F0000-F2016	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Timer Contacts*	TS0000-TS2047 TS0000-TS2032 TS0000-TS2016	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Timer Coils*	TC0000-TC2047 TC0000-TC2032 TC0000-TC2016	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Counter Contacts*	CS0000-CS1023 CS0000-CS1008	**Boolean** Short, Word, BCD	Read/Write

Device Type	Range	Data Type	Access
	CS0000-CS0992	Long, DWord, LBCD	
Counter Coils*	CC0000-CC1023 CC0000-CC1008 CC0000-CC0992	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write

* Users can specify a Long data type by appending a space and an "L" to the address. For example, "CS0000" would be entered as "CS0000 L". This does not apply to arrays or bit accessed registers.

Note: All Boolean device types can be accessed as Short, Word, BCD, Long, DWord and LBCD; however, the device must be addressed on a 16-bit boundary.

Device Type	Range	Data Type	Access
Timer Value	TN0000-TN2047	**Short**, Word, BCD	Read/Write
Counter Value	CN0000-CN1023	Short, **Word**, BCD	Read/Write
Data Registers***	D0000-D8191 D0000-D8190 D0000-D8188	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Data Register Bit Access	D0000.00-D8191.15* D0000.00-D8190.31*	**Short**, Word, BCD, Boolean Long, DWord, LBCD	Read/Write
Data Registers String Access HiLo Byte Ordering	DSH00000.002-DSH08190.002 DSH00000.128-DSH08127.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Data Registers String Access LoHi Byte Ordering	DSL00000.002–DSL08190.002 DSL00000.128-DSL08127.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Special Data Registers***	D9000-D9255 D9000-D9254 D9000-D9252	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read Only
Data Register Bit Access	D9000.00-D9255.15* D9000.00-D9254.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read Only
Link Registers***	W0000-W1FFF (Hex) W0000-W1FFE (Hex) W0000-W1FFC (Hex)	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write

Device Type	Range	Data Type	Access
Link Register Bit Access	W0000.00-W1FFF.15* W0000.00-W1FFE.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
Link Registers String Access HiLo Byte Ordering	WSH0000.002-WSH1FFE.002 WSH0000.128-WSH1FBF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Link Registers String Access LoHi Byte Ordering	WSL0000.002-WSL1FFE.002 WSL0000.128-WSL1FBF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
File Register***	R0000-R8191 R0000-R8190 R0000-R8188	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
File Register Bit Access	R0000.00-R8191.15* R0000.00-R8190.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
File Registers String Access HiLo Byte Ordering	RSH00000.002-RSH08190.002 RSH00000.128-RSH08127.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
File Registers String Access LoHi Byte Ordering	RSL00000.002-RSL08190.002 RSL00000.128-RSL08127.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write

* For register memory, the data types Short, Word, BCD, DWord, Long, LBCD and Boolean may append an optional ".bb" (dot bit) or ":bb" (colon bit) to the address to reference a bit in a particular value. The valid ranges for the optional bit are 0-15 for Short, Word, BCD, Boolean; and 0-31 for Long, DWord and LBCD. Strings use the bit number to specify length. The valid length of a string in D memory is 2 to 128 bytes. The string length must be an even number. Float types do not support bit operations. The bit number is always in decimal notation.

** When accessing register memory as Boolean, a bit number is required.

*** Users can specify a Long data type by appending a space and an "L" to the address. For example, "CS0000" would be entered as "CS0000 L". This does not apply to arrays or bit accessed registers.

Array Access

Many device types can be accessed as arrays. Boolean arrays and Date arrays are not supported. The default array tag for device types is Word. The size of the array depends on both the data type and the device type. Arrays may be one or two dimensions and the total number of Words being requested cannot exceed the Max Read Block Size specified for the device.

Note: An array is created when array notation is appended onto a normal device reference.

Examples

1. D100 [4] Single dimension includes the following register addresses: D100, D101, D102, D103.
2. M016 [3][4] Two Dimensions includes the following device addresses as words: M016, M032, M048, M064, M080, M096, M112, M128, M144, M160, M176, M192 3 rows x 4 columns = 12 words 12 x 16 (word) =192 total bits.

Additional Device Examples

1. Access X device memory as Word: X??? where the ??? is a hex number on 16-bit boundaries such as 010, 020, 030, and so forth.
2. Access M device memory as Long: M???? where the ???? is a decimal number on 16-bit boundaries such as 0, 16, 32, 48, and so forth.

Mitsubishi FX3U Series Address Descriptions

The default data types for dynamically defined tags are shown in **bold**.

Device Type	Range	Data Type	Access
Inputs*	X000-X377 (Oct) X000-X360 (Oct) X000-X340 (Oct)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Outputs*	Y000-Y377 (Oct) Y000-Y360 (Oct) Y000-Y340 (Oct)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Internal Relays*	M0000-M7679 M0000-M7664 M0000-M7648	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Special Int. Relays*	M8000-M8511 M8000-M8496 M8000-M8480	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Step Relays*	S0000-S4095 S0000-S4080 S0000-S4064	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Timer Contacts*	TS000-TS511 TS000-TS496 TS000-TS480	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Counter Contacts*	CS000-CS255 CS000-CS240 CS000-CS224	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write

* Users can specify a Long data type by appending a space and an "L" to the address. For example, "CS0000" would be entered as "CS0000 L". This does not apply to arrays or bit accessed registers.

Note: All Boolean device types can be accessed as Short, Word, BCD, Long, DWord and LBCD; however, the device must be addressed on a 16-bit boundary.

Device Type	Range	Data Type	Access
Timer Value	TN000-TN511	**Short**, Word, BCD	Read/Write
Counter Value***	CN000-CN199 CN200-CN255	Short, **Word**, BCD Long, **DWord**, LBCD	Read/Write
Data Registers***	D0000-D7999 D0000-D7998 D0000-D7996	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Data Register Bit Access	D0000.00-D7999.15* D0000.00-D7998.31*	**Short**, Word, BCD, Boolean Long, DWord, LBCD	Read/Write
Data Registers String Access HiLo Byte Ordering	DSH0000.002-DSH7998.002 DSH0000.128-DSH7935.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Data Registers String Access LoHi Byte Ordering	DSL0000.002-DSL7998.002 DSL0000.128-DSL7935.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Special Data Registers***	D8000-D8511 D8000-D8510 D8000-D8508	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Special Data Register Bit Access	D8000.00-D8511.15* D8000.00-D8510.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
File Register***	R00000-R32767 R00000-R32766 R00000-R32764	**Short**, Word, BCD Long, DWord,	Read/Write

Device Type	Range	Data Type	Access
		LBCD, Float, Date Double	
File Register Bit Access	R00000.00-R32767.15* R00000.00-R32766.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
File Registers String Access HiLo Byte Ordering	RSH00000.002-RSH32766.002 RSH00000.128-RSH32703.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
File Registers String Access LoHi Byte Ordering	RSL00000.002-RSL32766.002 RSL00000.128-RSL32703.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write

* For register memory, the data types Short, Word, BCD, DWord, Long, LBCD and Boolean append an optional ".bb" (dot bit) or ":bb" (colon bit) to the address to reference a bit in a particular value. The valid ranges for the optional bit are 0-15 for Short, Word, BCD and Boolean; and 0-31 for Long, DWord and LBCD. Strings use the bit number to specify length. The valid length of a string is 2 to 128 bytes. The string length must be an even number. Float types do not support bit operations. The bit number is always in decimal notation.
** When accessing register memory as Boolean, a bit number is required.
*** Users can specify a Long data type by appending a space and an "L" to the address. For example, "CS0000" would be entered as "CS0000 L". This does not apply to arrays or bit accessed registers.

Array Access

Many device types can be accessed as arrays. Boolean arrays and Date arrays are not supported. The default array tag for device types is Word. The size of the array depends on both the data type and the device type. Arrays may be one or two dimensions and the total number of Words being requested cannot exceed the Max Read Block Size specified for the device.

Notes:

1. An array is created when array notation is appended onto a normal device reference.
2. Due to a limit of the protocol, the largest bit memory array that can be written to is 10 Words/Shorts/BCDs (or 5 DWords/Longs/LBCDs). Although this limit differs from the largest bit memory array that can be read (32 words), the maximum Read/Write array size for register memory type is the same (64 words).

Examples

1. D100 [4] Single dimension includes the following register addresses: D100, D101, D102, D103.
2. M016 [3][4] Two Dimensions includes the following device addresses as words: M016, M032, M048, M064, M080, M096, M112, M128, M144, M160, M176, M192 3 rowsx4 columns=12 words 12 x 16 (word) = 192 total bits.

Additional Device Examples

1. Access M device memory as Long: M???? where the ???? is a decimal number on 16-bit boundaries such as 0, 16, 32, 48, and so forth.
2. Access Y device memory as Short: Y??? where the ??? is an Octal number on 16-bit boundaries such as 020, 040, 060, and so forth.

Mitsubishi L Series Address Descriptions

The default data types for dynamically defined tags are shown in **bold**.

Device Type	Range	Data Type	Access
Inputs*	X0000-X3FFF (Hex) X0000-X3FF0 (Hex) X0000-X3FE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Direct Inputs*	DX0000-DX3FFF (Hex) DX0000-DX3FF0 (Hex) DX0000-DX3FE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Outputs*	Y0000-Y3FFF (Hex) Y0000-Y3FF0 (Hex) Y0000-Y3FE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Direct Outputs*	DY0000-DY3FFF (Hex) DY0000-DY3FF0 (Hex) DY0000-DY3FE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Link Relays*	B0000-BEA6F (Hex) B0000-BEA60 (Hex) B0000-BEA50 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Special Link Relays*	SB0000-SB7D0F (Hex) SB0000-SB7D00 (Hex) SB0000-SB7CF0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Internal Relays*	M0000-M60015 M0000-M60000 M0000-M59984	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Special Int. Relays*	SM0000-SM2047 SM0000-SM2032 SM0000-SM2016	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Latch Relays*	L0000-L32015 L0000-L32000 L0000-L31984	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Annunciator Relays*	F0000-F32015 F0000-F32000	**Boolean** Short, Word, BCD	Read/Write

Device Type	Range	Data Type	Access
	F0000-F31984	Long, DWord, LBCD	
Edge Relays*	V0000-V32015 V0000-V32000 V0000-V31984	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Step Relays*	S0000-S16383 S0000-S16368 S0000-S16352	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Timer Contacts*	TS0000-TS32015 TS0000-TS32000 TS0000-TS31984	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Timer Coils*	TC0000-TC32015 TC0000-TC32000 TC0000-TC31984	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Integrating Timer Contacts*	SS0000-SS2047 SS0000-SS2032 SS0000-SS2016	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Integrating Timer Coils*	SC0000-SC2047 SC0000-SC2032 SC0000-SC2016	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Counter Contacts*	CS0000-CS32015 CS0000-CS32000 CS0000-CS31984	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Counter Coils*	CC0000-CC32015 CC0000-CC32000 CC0000-CC31984	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write

* Users can specify a Long data type by appending a space and an "L" to the address. For example, "CS0000" would be entered as "CS0000 L". This does not apply to arrays or bit accessed registers.

Note: All Boolean device types can be accessed as Short, Word, BCD, Long, DWord and LBCD; however, the device must be addressed on a 16-bit boundary.

Device Type	Range	Data Type	Access
Timer Value	TN0000-TN32000	**Short**, Word, BCD	Read/Write
Integrating Timer Value	SN0000-SN2047	**Short**, Word, BCD	Read/Write
Counter Value	CN0000-CN32000	Short, **Word**, BCD	Read/Write
Data Registers***	D0000000-D4184063 D0000000-D4184062 D0000000-D4184060 ***See Also:*** *Extended Registers*	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write

Device Type	Range	Data Type	Access
Data Register Bit Access	D0000000.00–D4184063.15* D0000000.00–D4184062.31* **See Also:** Extended Registers	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
Data Registers String Access HiLo Byte Ordering	DSH00000.002-DSH4184062.002 DSH00000.128-DSH4183999.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Data Registers String Access LoHi Byte Ordering	DSL00000.002-DSL4184062.002 DSL00000.128-DSL4183999.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Special Data Registers***	SD0000-SD2047 SD0000-SD2046 SD0000-SD2044	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Data Register Bit Access	SD0000.00-SD2047.15* SD0000.00-SD2046.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
Link Registers***	W0000-W3FD7FF (Hex) W0000-W3FD7FE (Hex) W0000-W3FD7FC (Hex) **See Also:** Extended Registers	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Link Register Bit Access	W0000.00-W3FD7FF.15* W0000.00-W3FD7FE.31* **See Also:** Extended Registers	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
Link Registers String Access HiLo Byte Ordering	WSH0000.002-WSH3FD7FE.002 WSH0000.128-WSH3FD7BF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Link Registers	WSL0000.002-WSL3FD7FE.002	**String**	Read/Write

Device Type	Range	Data Type	Access
String Access LoHi Byte Ordering	WSL0000.128-WSL3FD7BF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.		
Special Link Registers***	SW0000-SW7D00 (Hex) SW0000-SW7CFF (Hex) SW0000-SW7CFD (Hex)	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Link Register Bit Access	SW0000.00-SW7D00.15* SW0000.00-SW7CFF.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
File Register***	R00000-R32767 R00000-R32766 R00000-R32764 ZR0000-ZR3FD7FF (Hex) ZR0000-ZR3FD7FE (Hex) ZR0000-ZR3FD7FC (Hex)	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double **Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
File Register Bit Access	R00000.00-R32767.15* R00000.00-R32766.31* ZR0000.00-ZR3FD7FF.15* ZR0000.00-ZR3FD7FE.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD **Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
File Registers String Access HiLo Byte Ordering	RSH00000.002-RSH32766.002 RSH00000.128-RSH32703.128 ZRSH0000.002-ZRSH3FD7FE.002 ZRSH0000.128-ZRSH3FD7BF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String** **String**	Read/Write

Device Type	Range	Data Type	Access
File Registers String Access LoHi Byte Ordering	RSL00000.002-RSL32766.002 RSL00000.128-RSL32703.128 ZRSL0000.002-ZRSL3FD7FE.002 ZRSL0000.128-ZRSL3FD7BF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String** **String**	Read/Write
Index Registers***	Z00-Z20 Z00-Z19 Z00-Z17	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Index Register Bit Access	Z00.00-Z20.15* Z00.00-Z19.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write

* For register memory, the data types Short, Word, BCD, DWord, Long, LBCD and Boolean may append an optional ".bb" (dot bit) or ":bb" (colon bit) to the address to reference a bit in a particular value. The valid ranges for the optional bit are 0-15 for Short, Word, BCD and Boolean; and 0-31 for Long, DWord and LBCD. Strings use the bit number to specify length. The valid length of a string in D memory is 2 to 128 bytes. The string length must be an even number. Float types do not support bit operations. The bit number is always in decimal notation.
** When accessing register memory as Boolean, a bit number is required.
*** Users can specify a Long data type by appending a space and an "L" to the address. For example, "CS0000" would be entered as "CS0000 L". This does not apply to arrays or bit accessed registers.

Extended Registers

The extended range for Data Registers is D12288 to D4184063. The extended range for Link Registers is W3FFF (Hex) to W3FD7FF (Hex). These must be configured on the device.

Array Access

Many device types can be accessed as arrays. Boolean arrays and Date arrays are not supported. The default array tag for device types is Word. The size of the array depends on both the data type and the device type. Arrays may be one or two dimensions and the total number of Words being requested cannot exceed the Max Read Block Size specified for the device.

Note: An array is created when array notation is appended onto a normal device reference.

Examples:

1. D100 [4] Single dimension includes the following register addresses: D100, D101, D102, D103.
2. M016 [3][4] Two Dimensions includes the following device addresses as words: M016, M032, M048, M064, M080, M096, M112, M128, M144, M160, M176, M192 3 rows x 4 columns =12 words 12 x 16 (word) =192 total bits.

Additional Device Examples

1. Access X device memory as Word: X??? where the ??? is a hex number on 16-bit boundaries such as 010, 020, 030, and so forth.
2. Access M device memory as Long: M???? where the ???? is a decimal number on 16-bit boundaries such as 0, 16, 32, 48, and so forth.

Mitsubishi Q Series Address Descriptions

The default data types for dynamically defined tags are shown in **bold**.

Device Type	Range	Data Type	Access
Inputs*	X0000-X3FFF (Hex) X0000-X3FF0 (Hex) X0000-X3FE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Direct Inputs*	DX0000-DX3FFF (Hex) DX0000-DX3FF0 (Hex) DX0000-DX3FE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Outputs*	Y0000-Y3FFF (Hex) Y0000-Y3FF0 (Hex) Y0000-Y3FE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Direct Outputs*	DY0000-DY3FFF (Hex) DY0000-DY3FF0 (Hex) DY0000-DY3FE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Link Relays*	B0000-BEA6F (Hex) B0000-BEA60 (Hex) B0000-BEA50 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Special Link Relays*	SB0000-SB7D0F (Hex) SB0000-SB7D00 (Hex) SB0000-SB7CF0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Internal Relays*	M0000-M60015 M0000-M60000 M0000-M59984	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Special Int. Relays*	SM0000-SM2047 SM0000-SM2032 SM0000-SM2016	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Latch Relays*	L0000-L32015 L0000-L32000 L0000-L31984	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Annunciator Relays*	F0000-F32015 F0000-F32000 F0000-F31984	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Edge Relays*	V0000-V32015 V0000-V32000 V0000-V31984	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Step Relays*	S0000-S16383	**Boolean**	Read/Write

Device Type	Range	Data Type	Access
	S0000-S16368 S0000-S16352	Short, Word, BCD Long, DWord, LBCD	
Timer Contacts*	TS0000-TS32015 TS0000-TS32000 TS0000-TS31984	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Timer Coils*	TC0000-TC32015 TC0000-TC32000 TC0000-TC31984	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Integrating Timer Contacts*	SS0000-SS2047 SS0000-SS2032 SS0000-SS2016	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Integrating Timer Coils*	SC0000-SC2047 SC0000-SC2032 SC0000-SC2016	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Counter Contacts*	CS0000-CS32015 CS0000-CS32000 CS0000-CS31984	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Counter Coils*	CC0000-CC32015 CC0000-CC32000 CC0000-CC31984	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write

* Users can specify a Long data type by appending a space and an "L" to the address. For example, "CS0000" would be entered as "CS0000 L". This does not apply to arrays or bit accessed registers.

Note: All Boolean device types can be accessed as Short, Word, BCD, Long, DWord and LBCD; however, the device must be addressed on a 16-bit boundary.

Device Type	Range	Data Type	Access
Timer Value	TN0000-TN32000	**Short**, Word, BCD	Read/Write
Integrating Timer Value	SN0000-SN2047	**Short**, Word, BCD	Read/Write
Counter Value	CN0000-CN32000	Short, **Word**, BCD	Read/Write
Data Registers***	D0000000-D4184063 D0000000-D4184062 D0000000-D4184060 ***See Also:*** *Extended Registers*	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Data Register Bit Access	D0000000.00–D4184063.15* D0000000.00–D4184062.31* ***See Also:*** *Extended Registers*	**Short**, Word, BCD, Boolean** Long, DWord,	Read/Write

Device Type	Range	Data Type	Access
		LBCD	
Data Registers String Access HiLo Byte Ordering	DSH00000.002-DSH4184062.002 DSH00000.128-DSH4183999.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Data Registers String Access LoHi Byte Ordering	DSL00000.002-DSL4184062.002 DSL00000.128-DSL4183999.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Special Data Registers***	SD0000-SD2047 SD0000-SD2046 SD0000-SD2044	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Data Register Bit Access	SD0000.00-SD2047.15* SD0000.00-SD2046.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
Link Registers***	W0000-W3FD7FF (Hex) W0000-W3FD7FE (Hex) W0000-W3FD7FC (Hex) ***See Also:*** *Extended Registers*	**Short**, Word, BCD Long, DWord, LBCD, Float, Date, Double	Read/Write
Link Register Bit Access	W0000.00-W3FD7FF.15* W0000.00-W3FD7FE.31* ***See Also:*** *Extended Registers*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
Link Registers String Access HiLo Byte Ordering	WSH0000.002-WSH3FD7FE.002 WSH0000.128-WSH3FD7BF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Link Registers String Access LoHi Byte Ordering	WSL0000.002-WSL3FD7FE.002 WSL0000.128-WSL3FD7BF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write

Device Type	Range	Data Type	Access
Special Link Registers***	SW0000-SW7D00 (Hex) SW0000-SW7CFF (Hex) SW0000-SW7CFD (Hex)	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Link Register Bit Access	SW0000.00-SW7D00.15* SW0000.00-SW7CFF.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
File Register***	R00000-R32767 R00000-R32766 R00000-R32764 ZR0000-ZR3FD7FF (Hex) ZR0000-ZR3FD7FE (Hex) ZR0000-ZR3FD7FC (Hex)	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double **Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
File Register Bit Access	R00000.00-R32767.15* R00000.00-R32766.31* ZR0000.00-ZR3FD7FF.15* ZR0000.00-ZR3FD7FE.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD **Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
File Registers String Access HiLo Byte Ordering	RSH00000.002-RSH32766.002 RSH00000.128-RSH32703.128 ZRSH0000.002-ZRSH3FD7FE.002 ZRSH0000.128-ZRSH3FD7BF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String** **String**	Read/Write
File Registers String Access LoHi Byte Ordering	RSL00000.002-RSL32766.002 RSL00000.128-RSL32703.128 ZRSL0000.002-ZRSL3FD7FE.002 ZRSL0000.128-ZRSL3FD7BF.128	**String** **String**	Read/Write

Device Type	Range	Data Type	Access
	The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.		
Index Registers***	Z00-Z20 Z00-Z19 Z00-Z17	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Index Register Bit Access	Z00.00-Z20.15* Z00.00-Z19.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write

* For register memory, the data types Short, Word, BCD, DWord, Long, LBCD and Boolean may append an optional ".bb" (dot bit) or ":bb" (colon bit) to the address to reference a bit in a particular value. The valid ranges for the optional bit are 0-15 for Short, Word, BCD and Boolean; and 0-31 for Long, DWord and LBCD. Strings use the bit number to specify length. The valid length of a string in D memory is 2 to 128 bytes. The string length must be an even number. Float types do not support bit operations. The bit number is always in decimal notation.
** When accessing register memory as Boolean, a bit number is required.
*** Users can specify a Long data type by appending a space and an "L" to the address. For example, "CS0000" would be entered as "CS0000 L". This does not apply to arrays or bit accessed registers.

Extended Registers

The extended range for Data Registers is D12288 to D4184063. The extended range for Link Registers is W3FFF (Hex) to W3FD7FF (Hex). These must be configured on the device.

Array Access

Many device types can be accessed as arrays. Boolean arrays and Date arrays are not supported. The default array tag for device types is Word. The size of the array depends on both the data type and the device type. Arrays may be one or two dimensions and the total number of Words being requested cannot exceed the Max Read Block Size specified for the device.

Note: An array is created when array notation is appended onto a normal device reference.

Examples:

1. D100 [4] Single dimension includes the following register addresses: D100, D101, D102, D103.
2. M016 [3][4] Two Dimensions includes the following device addresses as words: M016, M032, M048, M064, M080, M096, M112, M128, M144, M160, M176, M192 3 rows x 4 columns =12 words 12 x 16 (word) =192 total bits.

Additional Device Examples

1. Access X device memory as Word: X??? where the ??? is a hex number on 16-bit boundaries such as 010, 020, 030, and so forth.
2. Access M device memory as Long: M???? where the ???? is a decimal number on 16-bit boundaries such as 0, 16, 32, 48, and so forth.

Mitsubishi iQ-R Series Address Descriptions

The default data types for dynamically defined tags are shown in **bold**.

Device Type	Range	Data Type	Access
Inputs*	X0000-XFFFF (Hex) X0000-XFFF0 (Hex) X0000-XFFE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Direct Inputs*	DX0000-DXFFFF (Hex) DX0000-DXFFF0 (Hex) DX0000-DXFFE0 (Hex))	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Outputs*	Y0000-YFFFF (Hex) Y0000-YFFF0 (Hex) Y0000-YFFE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Direct Outputs*	DY0000-DYFFFF (Hex) DY0000-DYFFF0 (Hex) DY0000-DYFFE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Link Relays*	B0000-B7FFFFFF (Hex) B0000-B7FFFFF0 (Hex) B0000-B7FFFFE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Special Link Relays*	SB0000-SB7FFFFFF (Hex) SB0000-SB7FFFFF0 (Hex) SB0000-SB7FFFFE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Internal Relays*	M0000-M2147483647 M0000-M2147483632 M0000-M2147483616	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Special Int. Relays*	SM0000-SM65535 SM0000-SM65520 SM0000-SM65504	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Latch Relays*	L0000-L66535 L0000-L65520 L0000-L65504	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Annunciator Relays*	F0000-F65535 F0000-F65520 F0000-F65504	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Edge Relays*	V0000-V65535 V0000-V65520 V0000-V65504	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Step Relays*	S0000-S65535 S0000-S65520 S0000-S65504	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write

Device Type	Range	Data Type	Access
Timer Contacts*	TS0000-TS16777215 TS0000-TS16777200 TS0000-TS16777184	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Timer Coils*	TC0000-TC16777215 TC0000-TC16777200 TC0000-TC16777184	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Retentive Timer Contacts*	STS000-STS16777215 STS000-STS16777200 STS000-STS16777184	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Retentive Timer Coils*	SC000-SC16777215 SC000-SC16777200 SC000-SC16777184	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Counter Contacts*	CS0000-CS16777215 CS0000-CS16777200 CS0000-CS16777184	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Counter Coils*	CC0000-CC16777215 CC0000-CC16777200 CC0000-CC16777184	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write

* Users can specify a Long data type by appending a space and an "L" to the address. For example, "CS0000" would be entered as "CS0000 L". This does not apply to arrays or bit accessed registers.

Note: All Boolean device types can be accessed as Short, Word, BCD, Long, DWord and LBCD; however, the device must be addressed on a 16-bit boundary.

Device Type	Range	Data Type	Access
Timer Value	TN0000-TN16777215	**Short**, Word, BCD	Read/Write
Retentive Timer Value	SN0000-SN16777215	**Short**, Word, BCD	Read/Write
Counter Value	CN0000-CN16777215	Short, **Word**, BCD	Read/Write
Data Registers***	D0000000-D16777215 D0000000-D16777214 D0000000-D16777212 ***See Also:*** *Extended Registers*	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Data Register Bit Access	D0000000.00–D16777215.15* D0000000.00–16777212.31* ***See Also:*** *Extended Registers*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
Data	DSH00000.002-DSH16777214.002	**String**	Read/Write

Device Type	Range	Data Type	Access
Registers String Access HiLo Byte Ordering	DSH00000.128-DSH16777151.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.		
Data Registers String Access LoHi Byte Ordering	DSL00000.002-DSL16777214.002 DSL00000.128-DSL16777151.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Special Data Registers***	SD0000-SD65535 SD0000-SD65534 SD0000-SD65532	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Data Register Bit Access	SD0000.00-SD65535.15* SD0000.00-SD65534.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
Link Registers***	W0000-WFFFFFF(Hex) W0000-WFFFFFE(Hex) W0000-WFFFFFC(Hex) ***See Also:*** *Extended Registers*	**Short**, Word, BCD Long, DWord, LBCD, Float, Date, Double	Read/Write
Link Register Bit Access	W0000.00-WFFFFFF.15* W0000.00-WFFFFFE.31* ***See Also:*** *Extended Registers*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
Link Registers String Access HiLo Byte Ordering	WSH0000.002-WSHFFFFFE.002 WSH0000.128-WSH3FD7BF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Link Registers String Access LoHi Byte Ordering	WSL0000.002-WSLFFFFFE.002 WSL0000.128-WSLFFFFBF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Special Link Registers***	SW0000-SWFFFFFF (Hex) SW0000-SWFFFFFE (Hex)	**Short**, Word, BCD	Read/Write

Device Type	Range	Data Type	Access
	SW0000-SWFFFFFC (Hex)	Long, DWord, LBCD, Float, Date Double	
Link Register Bit Access	SW0000.00-SWFFFFFF.15* SW0000.00-SWFFFFFE.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
File Register***	R00000-R65535 R00000-R65534 R00000-R65532 ZR0000-ZRFFFFFF (Hex) ZR0000-ZRFFFFFE (Hex) ZR0000-ZRFFFFFC (Hex)	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double **Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
File Register Bit Access	R00000.00-R65535.15* R00000.00-R65534.31* ZR0000.00-ZRFFFFFF.15* ZR0000.00-ZRFFFFFE.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD **Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
File Registers String Access HiLo Byte Ordering	RSH00000.002-RSH65534.002 RSH00000.128-RSH65471.128 ZRSH0000.002-ZRSHFFFFFE.002 ZRSH0000.128-ZRSHFFFFBF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String** **String**	Read/Write
File Registers String Access LoHi Byte Ordering	RSL00000.002-RSL65534.002 RSL00000.128-RSL16777151.128 ZRSL0000.002-ZRSLFFFFFE.002 ZRSL0000.128-ZRSLFFFFBF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String** **String**	Read/Write

Device Type	Range	Data Type	Access
Index Registers***	Z00-Z255 Z00-Z254 Z00-Z252	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Index Register Bit Access	Z00.00-Z255.15* Z00.00-Z254.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write

* For register memory, the data types Short, Word, BCD, DWord, Long, LBCD and Boolean may append an optional ".bb" (dot bit) or ":bb" (colon bit) to the address to reference a bit in a particular value. The valid ranges for the optional bit are 0-15 for Short, Word, BCD and Boolean; and 0-31 for Long, DWord and LBCD. Strings use the bit number to specify length. The valid length of a string in D memory is 2 to 128 bytes. The string length must be an even number. Float types do not support bit operations. The bit number is always in decimal notation.
** When accessing register memory as Boolean, a bit number is required.
*** Users can specify a Long data type by appending a space and an "L" to the address. For example, "CS0000" would be entered as "CS0000 L". This does not apply to arrays or bit accessed registers.

Extended Registers

The extended range for Data Registers is D12288 to D16777215. The extended range for Link Registers is W3FFF (Hex) to WFFFFFF (Hex). These must be configured on the device.

Array Access

Many device types can be accessed as arrays. Boolean arrays and Date arrays are not supported. The default array tag for device types is Word. The size of the array depends on both the data type and the device type. Arrays may be one or two dimensions and the total number of Words being requested cannot exceed the Max Read Block Size specified for the device.

Note: An array is created when array notation is appended onto a normal device reference.

Examples:

1. D100 [4] Single dimension includes the following register addresses: D100, D101, D102, D103.
2. M016 [3][4] Two Dimensions includes the following device addresses as words: M016, M032, M048, M064, M080, M096, M112, M128, M144, M160, M176, M192 3 rows x 4 columns =12 words 12 x 16 (word) =192 total bits.

Additional Device Examples

1. Access X device memory as Word: X??? where the ??? is a hex number on 16-bit boundaries such as 010, 020, 030, and so forth.
2. Access M device memory as Long: M???? where the ???? is a decimal number on 16-bit boundaries such as 0, 16, 32, 48, and so forth.

Mitsubishi iQ-F Series Address Descriptions

The default data types for dynamically defined tags are shown in **bold**.

Device Type	Range	Data Type	Access
Inputs*	X0000-X177777 (Octal) X0000-X177760 (Octal) X0000-X177740 (Octal)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Outputs*	Y0000-Y177777 (Octal) Y0000-Y177760 (Octal) Y0000-Y177740 (Octal)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Link Relays*	B0000-BFFFF (Hex) B0000-BFFF0 (Hex) B0000-BFFE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Special Link Relays*	SB0000-SBFFFF (Hex) SB0000-SBFFF0 (Hex) SB0000-SBFFE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Internal Relays*	M0000-M65535 M0000-M65520 M0000-M65504	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Special Int. Relays*	SM0000-SM65535 SM0000-SM65520 SM0000-SM65504	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Latch Relays*	L0000-L66535 L0000-L65520 L0000-L65504	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Annunciator Relays*	F0000-F65535 F0000-F65520 F0000-F65504	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Step Relays*	S0000-S65535 S0000-S65520 S0000-S65504	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Timer Contacts*	TS0000-TS65535 TS0000-TS65520 TS0000-TS65504	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Timer Coils*	TC0000-TC65535 TC0000-TC65520 TC0000-TC65504	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Retentive Timer Contacts*	STS0000-STS65535 STS0000-STS65520 STS0000-STS65504	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Retentive Timer Coils*	SC000-SC16777215 SC000-SC16777200 SC000-SC16777184	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Counter Contacts*	CS0000-CS65535 CS0000-CS65520 CS0000-CS65504	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write

Device Type	Range	Data Type	Access
Counter Coils*	CC0000-CC65535 CC0000-CC65520 CC0000-CC65504	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write

* Users can specify a Long data type by appending a space and an "L" to the address. For example, "CS0000" would be entered as "CS0000 L". This does not apply to arrays or bit accessed registers.

Note: All Boolean device types can be accessed as Short, Word, BCD, Long, DWord and LBCD; however, the device must be addressed on a 16-bit boundary.

Device Type	Range	Data Type	Access
Timer Value	TN0000-TN65535	**Short**, Word, BCD	Read/Write
Retentive Timer Value	STN0000-STN65535	**Short**, Word, BCD	Read/Write
Counter Value	CN0000-CN65535	Short, **Word**, BCD	Read/Write
Data Registers***	D0000000-D65535 D0000000-D65534 D0000000-D65532 ***See Also:*** *Extended Registers*	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Data Register Bit Access	D0000000.00–D65535.15* D0000000.00–D65534.31* ***See Also:*** *Extended Registers*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
Data Registers String Access HiLo Byte Ordering	DSH00000.002-DSH65534.002 DSH00000.128-DSH65471.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Data Registers String Access LoHi Byte Ordering	DSL00000.002-DSL65534.002 DSL00000.128-DSL65471.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Special Data Registers***	SD0000-SD65535 SD0000-SD65534 SD0000-SD65532	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write

Device Type	Range	Data Type	Access
Data Register Bit Access	SD0000.00-SD65535.15* SD0000.00-SD65534.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
Link Registers***	W0000-WFFFF(Hex) W0000-WFFFE(Hex) W0000-WFFFC(Hex) ***See Also:*** *Extended Registers*	**Short**, Word, BCD Long, DWord, LBCD, Float, Date, Double	Read/Write
Link Register Bit Access	W0000.00-WFFFF.15* W0000.00-WFFFE.31* ***See Also:*** *Extended Registers*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
Link Registers String Access HiLo Byte Ordering	WSH0000.002-WSHFFFE.002 WSH0000.128-WSHFFBF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Link Registers String Access LoHi Byte Ordering	WSL0000.002-WSLFFFE.002 WSL0000.128-WSLFFBF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Special Link Registers***	SW0000-SWFFFF(Hex) SW0000-SWFFFE(Hex) SW0000-SWFFFC(Hex)	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Link Register Bit Access	SW0000.00-SWFFFF.15* SW0000.00-SWFFFE.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
File Register***	R00000-R65535 R00000-R65534 R00000-R65532	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write

Device Type	Range	Data Type	Access
File Register Bit Access	R00000.00-R65535.15* R00000.00-R65534.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
File Registers String Access HiLo Byte Ordering	RSH00000.002-RSH65534.002 RSH00000.128-RSH65471.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
File Registers String Access LoHi Byte Ordering	RSL00000.002-RSL65534.002 RSL00000.128-RSL65471.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Index Registers***	Z00-Z255 Z00-Z254 Z00-Z252	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Index Register Bit Access	Z00.00-Z255.15* Z00.00-Z254.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write

* For register memory, the data types Short, Word, BCD, DWord, Long, LBCD and Boolean may append an optional ".bb" (dot bit) or ":bb" (colon bit) to the address to reference a bit in a particular value. The valid ranges for the optional bit are 0-15 for Short, Word, BCD and Boolean; and 0-31 for Long, DWord and LBCD. Strings use the bit number to specify length. The valid length of a string in D memory is 2 to 128 bytes. The string length must be an even number. Float types do not support bit operations. The bit number is always in decimal notation.
** When accessing register memory as Boolean, a bit number is required.
*** Users can specify a Long data type by appending a space and an "L" to the address. For example, "CS0000" would be entered as "CS0000 L". This does not apply to arrays or bit accessed registers.

Extended Registers

The extended range for Data Registers is D12288 to D65535. The extended range for Link Registers is W3FFF (Hex) to WFFFF (Hex). These must be configured on the device.

Array Access

Many device types can be accessed as arrays. Boolean arrays and Date arrays are not supported. The default array tag for device types is Word. The size of the array depends on both the data type and the device type. Arrays may be one or two dimensions and the total number of Words being requested cannot exceed the Max Read Block Size specified for the device.

Note: An array is created when array notation is appended onto a normal device reference.

Examples:

1. D100 [4] Single dimension includes the following register addresses: D100, D101, D102, D103.
2. M016 [3][4] Two Dimensions includes the following device addresses as words: M016, M032, M048, M064, M080, M096, M112, M128, M144, M160, M176, M192 3 rows x 4 columns =12 words 12 x 16 (word) =192 total bits.

Additional Device Examples

1. Access X device memory as Word: X??? where the ??? is a hex number on 16-bit boundaries such as 010, 020, 030, and so forth.
2. Access M device memory as Long: M???? where the ???? is a decimal number on 16-bit boundaries such as 0, 16, 32, 48, and so forth.

Mitsubishi QnA Series Address Descriptions

The default data types for dynamically defined tags are shown in **bold**.

Device Type	Range	Data Type	Access
Inputs*	X0000-X3FFF (Hex) X0000-X3FF0 (Hex) X0000-X3FE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Direct Inputs*	DX0000-DX3FFF (Hex) DX0000-DX3FF0 (Hex) DX0000-DX3FE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Outputs*	Y0000-Y3FFF (Hex) Y0000-Y3FF0 (Hex) Y0000-Y3FE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Direct Outputs*	DY0000-DY3FFF (Hex) DY0000-DY3FF0 (Hex) DY0000-DY3FE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Link Relays*	B0000-BEA6F (Hex) B0000-BEA50 (Hex) B0000-BEA40 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Special Link Relays*	SB0000-SB7D0F (Hex) SB0000-SB7CF0 (Hex) SB0000-SB7CE0 (Hex)	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Internal Relays*	M0000-M60015 M0000-M59984 M0000-M59968	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Special Int. Relays*	SM0000-SM2047 SM0000-SM2032 SM0000-SM2016	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Latch Relays*	L0000-L32015 L0000-L31984 L0000-L31968	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write

Device Type	Range	Data Type	Access
Annunciator Relays*	F0000-F32015 F0000-F31984 F0000-F31968	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Edge Relays*	V0000-V32015 V0000-V31984 V0000-V31968	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Step Relays*	S0000-S16383 S0000-S16368 S0000-S16352	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Timer Contacts*	TS0000-TS32015 TS0000-TS31984 TS0000-TS31968	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Timer Coils*	TC0000-TC32015 TC0000-TC31984 TC0000-TC31968	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Integrating Timer Contacts*	SS0000-SS2047 SS0000-SS2032 SS0000-SS2016	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Integrating Timer Coils*	SC0000-SC2047 SC0000-SC2032 SC0000-SC2016	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Counter Contacts*	CS0000-CS32015 CS0000-CS31984 CS0000-CS31968	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write
Counter Coils*	CC0000-CC32015 CC0000-CC31984 CC0000-CC31968	**Boolean** Short, Word, BCD Long, DWord, LBCD	Read/Write

* Users can specify a Long data type by appending a space and an "L" to the address. For example, "CS0000" would be entered as "CS0000 L". This does not apply to arrays or bit accessed registers.

Note: All Boolean device types can be accessed as Short, Word, BCD, Long, DWord and LBCD; however, the device must be addressed on a 16-bit boundary.

Device Type	Range	Data Type	Access
Timer Value	TN0000-TN32000	**Short**, Word, BCD	Read/Write
Integrating Timer Value	SN0000-SN2047	**Short**, Word, BCD	Read/Write
Counter Value	CN0000-CN32000	Short, **Word**, BCD	Read/Write
Data Registers***	D0000000-D4184063 D0000000-D4184062 D0000000-D4184060 ***See Also:*** *Extended Registers*	**Short**, Word, BCD Long, DWord, LBCD, Float, Date	Read/Write

Device Type	Range	Data Type	Access
		Double	
Data Register Bit Access	D0000000.00–D4184063.15* D0000000.00–D4184062.31* **See Also:** Extended Registers	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
Data Registers String Access HiLo Byte Ordering	DSH00000.002-DSH4184062.002 DSH00000.128-DSH4183999.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Data Registers String Access LoHi Byte Ordering	DSL00000.002-DSL4184062.002 DSL00000.128-DSL4183999.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Special Data Registers***	SD0000-SD2047 SD0000-SD2046 SD0000-SD2044	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Data Register Bit Access	SD0000.00-SD2047.15* SD0000.00-SD2046.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
Link Registers***	W0000-W3FD7FF (Hex) W0000-W3FD7FE (Hex) W0000-W3FD7FC (Hex) **See Also:** Extended Registers	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Link Register Bit Access	W0000.00-W3FD7FF.15* W0000.00-W3FD7FE.31* **See Also:** Extended Registers	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
Link Registers String Access HiLo Byte Ordering	WSH0000.002-WSH3FD7FE.002 WSH0000.128-WSH3FD7BF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String**	Read/Write
Link Registers	WSL0000.002-WSL3FD7FE.002	**String**	Read/Write

Device Type	Range	Data Type	Access
String Access LoHi Byte Ordering	WSL0000.128-WSL3FD7BF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.		
Special Link Registers***	SW0000-SW7D00 (Hex) SW0000-SW7CFF (Hex) SW0000-SW7CFD (Hex)	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Link Register Bit Access	SW0000.00-SW7D00.15* SW0000.00-SW7CFF.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
File Register***	R00000-R32767 R00000-R32766 R00000-R32764 ZR0000-ZR3FD7FF (Hex) ZR0000-ZR3FD7FE (Hex) ZR0000-ZR3FD7FC (Hex)	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double **Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
File Register Bit Access	R00000.00-R32767.15* R00000.00-R32766.31* ZR0000.00-ZR3FD7FF.15* ZR0000.00-ZR3FD7FE.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD **Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write
File Registers String Access HiLo Byte Ordering	RSH00000.002-RSH32766.002 RSH00000.128-RSH32703.128 ZRSH0000.002-ZRSH3FD7FE.002 ZRSH0000.128-ZRSH3FD7BF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String** **String**	Read/Write

Device Type	Range	Data Type	Access
File Registers String Access LoHi Byte Ordering	RSL00000.002-RSL32766.002 RSL00000.128-RSL32703.128 ZRSL0000.002-ZRSL3FD7FE.002 ZRSL0000.128-ZRSL3FD7BF.128 The string length may also be specified using a colon. The string length must be between 2-128 bytes and even.	**String** **String**	Read/Write
Index Registers***	Z00-Z20 Z00-Z19 Z00-Z17	**Short**, Word, BCD Long, DWord, LBCD, Float, Date Double	Read/Write
Index Register Bit Access	Z00.00-Z20.15* Z00.00-Z19.31*	**Short**, Word, BCD, Boolean** Long, DWord, LBCD	Read/Write

* For register memory, the data types Short, Word, BCD, DWord, Long, LBCD and Boolean may append an optional ".bb" (dot bit) or ":bb" (colon bit) to the address to reference a bit in a particular value. The valid ranges for the optional bit are 0-15 for Short, Word, BCD and Boolean; and 0-31 for Long, DWord and LBCD. Strings use the bit number to specify length. The valid length of a string in D memory is 2 to 128 bytes. The string length must be an even number. Float types do not support bit operations. The bit number is always in decimal notation.
** When accessing register memory as Boolean, a bit number is required.
*** Users can specify a Long data type by appending a space and an "L" to the address. For example, "CS0000" would be entered as "CS0000 L". This does not apply to arrays or bit accessed registers.

Extended Registers

The extended range for Data Registers is D12288 to D4184063. The extended range for Link Registers is W3FFF (Hex) to W3FD7FF (Hex). These must be configured on the device.

Array Access

Many device types can be accessed as arrays. Boolean arrays and Date arrays are not supported. The default array tag for device types is Word. The size of the array depends on both the data type and the device type. Arrays may be one or two dimensions and the total number of Words being requested cannot exceed the Max Read Block Size specified for the device.

Note: An array is created when array notation is appended onto a normal device reference.

Examples:

1. D100 [4] Single dimension includes the following register addresses: D100, D101, D102, D103.

2. M016 [3][4] Two Dimensions includes the following device addresses as words: M016, M032, M048, M064, M080, M096, M112, M128, M144, M160, M176, M192 3 rows x 4 columns =12 words 12 x 16 (word) =192 total bits.

Additional Device Examples

1. Access X device memory as Word: X*???* where the *???* is a hex number on 16-bit boundaries such as 010, 020, 030, and so forth.
2. Access M device memory as Long: M*????* where the *????* is a decimal number on 16-bit boundaries such as 0, 16, 32, 48, and so forth.

Event Log Messages

The following information concerns messages posted to the Event Log pane in the main user interface. Consult the OPC server help on filtering and sorting the Event Log detail view. Server help contains many common messages, so should also be searched. Generally, the type of message (informational, warning) and troubleshooting information is provided whenever possible.

Unable to read from address block on device. The device reported an invalid address or an error. | Address block = '<address>' to '<address>'.

Error Type:
Error

Possible Cause:

1. An attempt has been made to read a non-existent location in the specified device.
2. An attempt has been made to read from an address in a device that is not located on the specified network node.

Possible Solution:

1. Verify or correct the tags assigned to addresses in the specified range on the device. Eliminate or update any that reference invalid locations.
2. Verify that the node ID referenced in the device address is correct.

Unable to read from device. The device returned a PC number error.

Error Type:
Error

Possible Cause:
The PC number entered for the device ID is invalid. This may occur if the MelsecNet station is not available.

Possible Solution:

1. If attempting to communicate with a PC located on MelsecNet, verify the PC number of the target PC.
2. If intending to communicate directly with the local PC with the Ethernet connection, specify a PC number of 255.

Note:
All tag reads fail until the PC number is corrected.

Unable to write to address on device. The device returned a PC number error. | Address = '<address>'.

Error Type:
Error

Possible Cause:
The PC number entered for the device ID is invalid. This may occur if the MelsecNet station is not available.

Possible Solution:
1. If attempting to communicate with a PC located on MelsecNet, verify the PC number of the target PC.
2. If intending to communicate directly with the local PC with the Ethernet connection, specify a PC number of 255.

Unable to write to address on device. The device reported an invalid address or an error. | Address = '<address>'.

Error Type:
Error

Possible Cause:
1. An attempt has been made to write a non-existent location in the specified device.
2. An attempt has been made to write from an address in a device that is not located on the specified network node.

Possible Solution:
1. Verify or correct the tags assigned to addresses in the specified range on the device. Eliminate or update any that reference invalid locations.
2. Verify that the node ID referenced in the device address is correct.

Unable to read from address on device. The device reported an invalid address or an error. | Address = '<address>'.

Error Type:
Error

Possible Cause:
1. An attempt has been made to read a non-existent location in the specified device.
2. An attempt has been made to read from an address in a device that is not located on the specified network node.

Possible Solution:
1. Verify or correct the tags assigned to addresses in the specified range on the device. Eliminate or update any that reference invalid locations.
2. Verify that the node ID referenced in the device address is correct.

Unable to read from address on device. Device returned an error. | Address = '<address>', Error code = <code>.

Error Type:
Error

Possible Cause:
Communication with the device succeeded, but the device reported a problem.

Possible Solution:
Consult the device documentation for information about the error code provided.

Unable to read from address block on device. Device returned an error. | Address block = '<address>' to '<address>', Error code = <code>.

Error Type:
Error

Possible Cause:
Communication with the device succeeded, but the device reported a problem.

Possible Solution:
Consult the device documentation for information about the error code provided.

Unable to write to address on device. Device returned error. | Address = '<address>', Error code = <code>.

Error Type:
Error

Possible Cause:
Communication with the device succeeded, but the device reported a problem.

Possible Solution:
Consult the device documentation for information about the error code provided.

Unable to read from address block on device. | Address block = '<address>' to '<address>'.

Error Type:
Error

Possible Cause:
The driver could not allocate the resources required to read from the device.

Possible Solution:
Shut down unnecessary applications and try again.

Unable to read from address on device. | Address = '<address>'.

Error Type:
Error

Possible Cause:
The driver could not allocate the resources required to read from the device.

Possible Solution:
Shut down unnecessary applications and try again.

Unable to write to address on device. Device must be configured to allow writes while in RUN mode. | Address = '<address>'.

Error Type:
Warning

Possible Cause:
The device is not configured to allow transactions during RUN mode.

Possible Solution:

1. For A-Series and QnA-Series PLCs, configure the AJ71E71 card to allow writes to occur during RUN by setting DIP switch 7 to the ON position.
2. For Q-Series and L-Series PLCs, use GX Developer to enable the setting "Enable Write at RUN time" in Ethernet Operations settings.

See Also:

1. A-Series PLC Setup
2. QnA-Series PLC Setup
3. Q-Series PLC Setup

Failed to synchronize time and date for device. | Retry interval = <number> (minutes).

Error Type:
Warning

Possible Cause:
The driver failed to write time and date data to the PLC.

Possible Solution:

1. Verify the cabling between the PC and the PLC device.
2. Verify that the specified communications parameters match those of the device.
3. Verify that the network ID given to the named device matches that of the actual device.

Note:

The driver automatically retries after the indicated time interval.

Appendix: PLC Setup

The hardware must be configured for Ethernet communications. For information on a specific hardware series, select a link from the list below.

The following is provided for convenience only. Refer to the manufacturer's documentation for current and official instructions.

A Series PLC Setup

Note: The following is provided for convenience only. Refer to the manufacturer's documentation for current and official instructions.

Hardware Settings

The DIP switches on the AJ71E71 Ethernet interface card must be set as follows.

- DIP switches 1-6 must be set to OFF.
- DIP switch 7 must be set to ON.
- DIP switch 8 must be set to OFF.

Ladder Program

The Mitsubishi A Series PLC requires that a ladder program be used to initialize the AJ71E71 or A1SJ71E71 Ethernet interface card and define the desired open system. TCP/IP and UDP open systems may be used with this driver. In the case of TCP/IP, error handling code should also be implemented.

Note: TCP/IP is less efficient than UDP and requires special ladder to handle network error recovery. Also, if planning to communicate with devices on a remote network, TCP/IP requires that multiple ports be configured in the relay device. Thus, UDP is recommended wherever possible. For more information, refer to **Multi-level Networks**.

Initialization Ladder

The following initialization code sets the IP address of the device and triggers execution of the open code. For this example, an IP of 192.168.111.123 (C0.A8.6F.7B Hex) is assumed.

```
| M9038                                  H              |
+-| |--+-------------------------[DMOV C0A86F7B D100]|
|      |                             H    K       K  |
+      +-------------------------[TO 0000 0  D100 2 ]|
|      |                                               |
+      +---------------------------------------[SET M40 ]|
| M40                                                  |
+-| |-------------------------------------------<Y0019>|
| X0019  Y0019                                         |
+-| |----| |---------------------------------[PLS M41 ]|
| M41                                                  |
+-| |-----------------------------------------[SET M42 ]|
|                                                      |
```

Open and Error Handling Ladder for TCP/IP

The following open and error handling code assumes TCP/IP communications, unpassive mode, on port 5001 (1389 Hex).

This code is for the first communications buffer of the AJ71E71 card. Similar code must be implemented for each additional buffer needed. Ensure that the proper interface bits are used as well as separate error handling bits and timers for each buffer.

Note: It is strongly recommended that users follow the code fragment as closely as possible. Without proper error handling and recovery on the PLC side of the connection, communications with the PLC may not be reestablished after a physical error, such as a cable break, occurs. Without the error handling represented here, PLC may have to be reset to reestablish communications.

```
| M42    X0010  Y0008                    H    K  H    K  |
+-| |---|/|----|/|-----+----------[TO 0000 16 8002 1 ]|
|                       |               H    K  H    K  |
+                       +----------[TO 0000 24 1389 1 ]|
|                       |                               |
+                       +--------------------[SET Y0008]|
| X0010                                                 |
+-| |-------------------------------------[PLF M50 ]|
| M50                                                   |
+-| |--+----------------------------------[RST Y0008]|
|      |                                                |
+      +-----------------------------------[RST M42 ]|
|      |                                                |
+      +-----------------------------------[SET M51 ]|
| M51                                             K20  |
+-| |-----------------------------------------<TO >|
| T0                                                    |
+-| |--+----------------------------------[RST M51 ]|
|      |                                                |
+      +-----------------------------------[SET M42 ]|
```

Given the ladder fragment shown here for TCP/IP port operation, the AJ71E71 will be forced to close and re-enable the port for a connection if the current connection is lost. This will occur 2 seconds after the error is detected as controlled by T0. Reloading the port mode and port number and the set of Y008 resets the port.

Open Ladder for UDP

The following open code assumes UDP communications on port 5000 (1388 Hex). The UDP open system requires that the destination address be specified. This would be the IP and port that the driver will use to

communicate with the PLC. To prevent issues with conflicting port usage, the Mitsubishi Ethernet Driver allows Windows to assign any unused UDP port to each device configured in the driver on startup. Thus, the port that the driver will use is not predictable. Therefore, the destination port must be configured in the PLC as "unspecified". This is done by entering FFFF (Hex) as shown below. The exact IP address that the driver will use may be specified. This example assumes 192.168.111.24 (C0.A8.6F.18 Hex). However, the destination may also be left as "unspecified" with 255.255.255.255 (FF.FF.FF.FF Hex).

Note: If a specific IP address is put into the ladder code, only the machine with that IP address will be able to communicate with the PLC via UDP. If the IP address is left as "unspecified," then any IP address can communicate with the PLC.

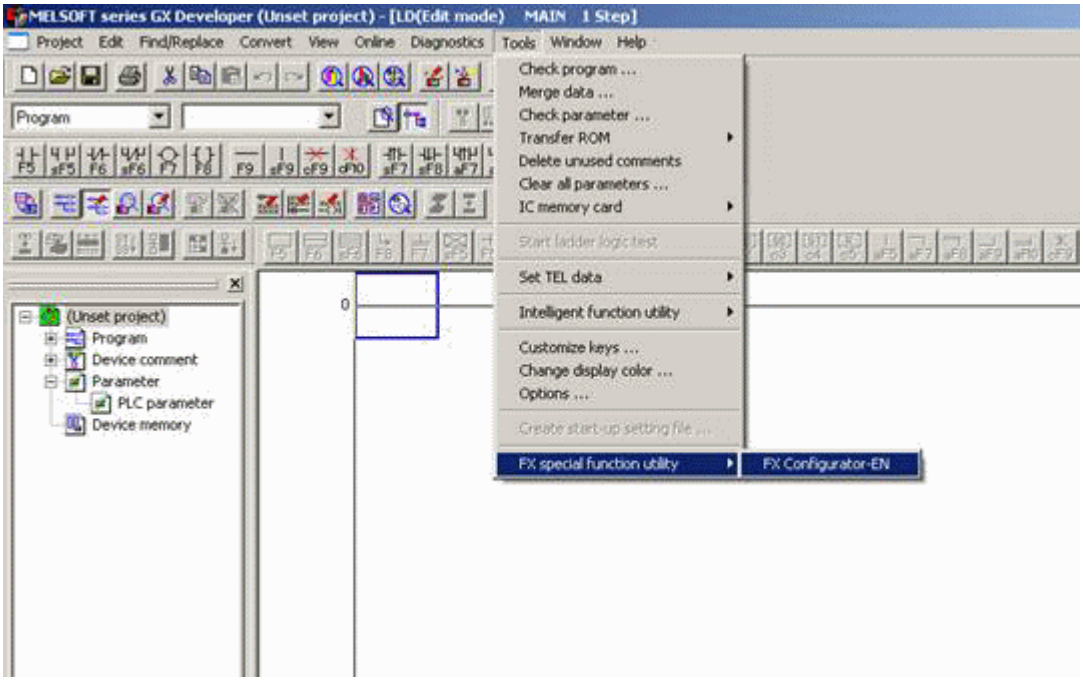
```
| M42   X0010  Y0008                  H    K  H    K  |
+-| |---|/|----|/|-----+----------[TO 0000 16 110  1 ]|
|                       |               H    K  H    K  |
+                       +----------[TO 0000 24 1388 1 ]|
|                       |               H    K  H    K  |
+                       +----------[TO 0000 25 6F18 1 ]|
|                       |               H    K  H    K  |
+                       +----------[TO 0000 26 C0A8 1 ]|
|                       |               H    K  H    K  |
+                       +----------[TO 0000 27 FFFF 1 ]|
|                       |                              |
+                       +--------------------[SET Y0008]|
```

FX3U Series PLC Setup

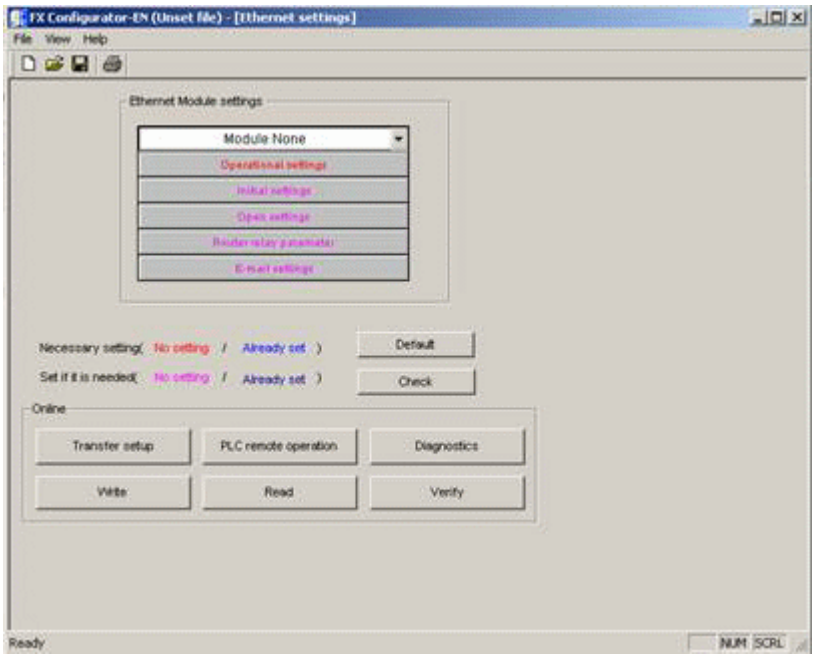
For the Mitsubishi Ethernet Driver to communicate with the FX3U PLC via the FX3U-ENET block, some network parameters have to be configured in the FX3U PLC. The Mitsubishi GXDeveloper-FX software is necessary for the following process.

Device Configuration

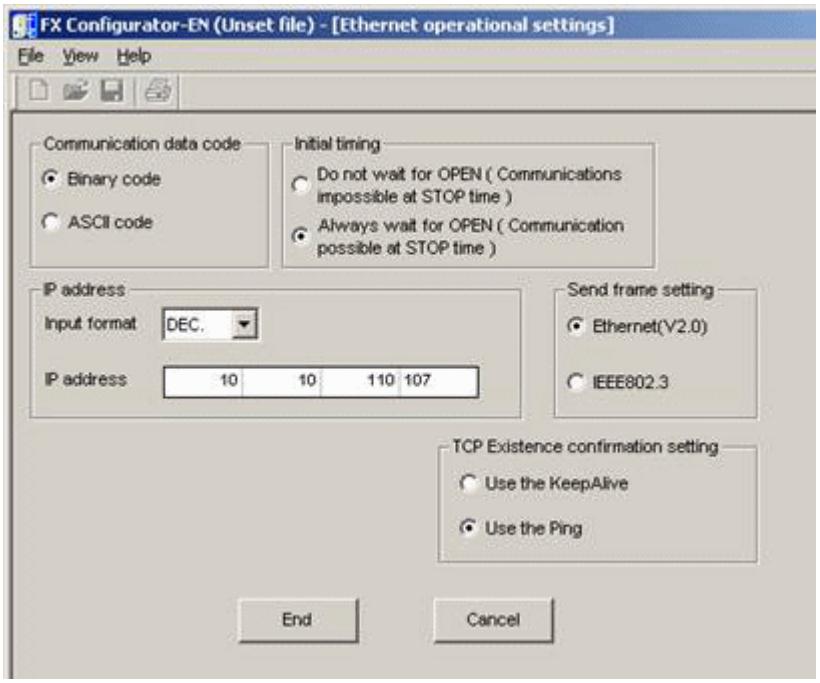
1. To start, create a new GXDeveloper project for a FX3U model. Then click **Tools** | **FX Special Function Utility**.
2. Next, select **FX Configurator-EN**.



Note: The **FX Configurator-EN** dialog should appear as shown below.



3. Next, specify the FX3U-ENET block's minimum required configuration information. Select a module from the first drop-down list and then click **Operational Settings**.

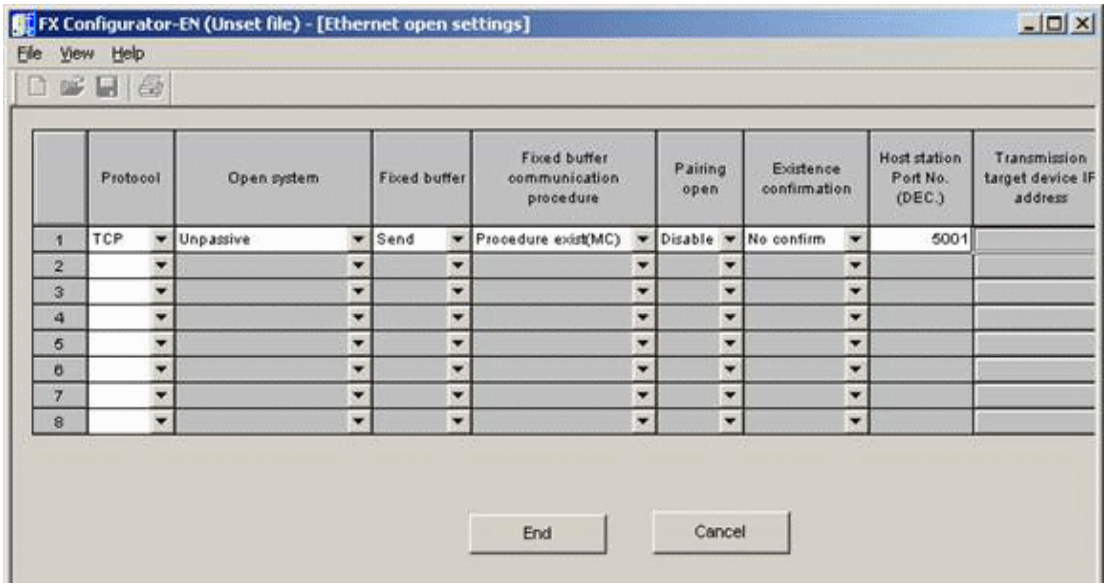


4. Specify the settings so that they appear similar to the ones shown above.
5. Click **End**.
6. In **FX Configurator-EN**, click **Open Settings**.
7. The open settings depend on the chosen IP protocol: TCP or UDP.

Open Settings for TCP

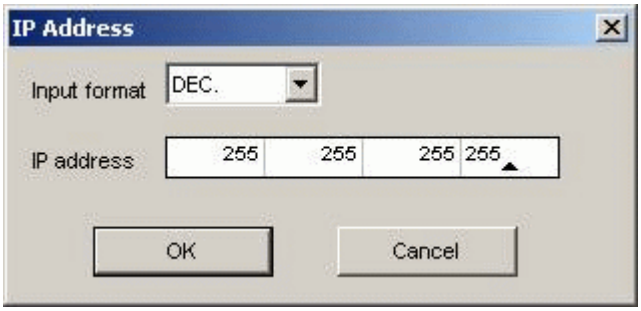
Enter **TCP** in the Protocol field. For simplicity, the **Unpassive** open system is recommended. By using the unpassive open system, the IP and port that the driver will use do not need to be configured. The **Procedure exist(MC)** communications procedure sets the correct protocol in the FX3U-ENET block to communicate with this driver. In the example below, **5001** (1389 Hex) is specified in the Host station Port No. field.

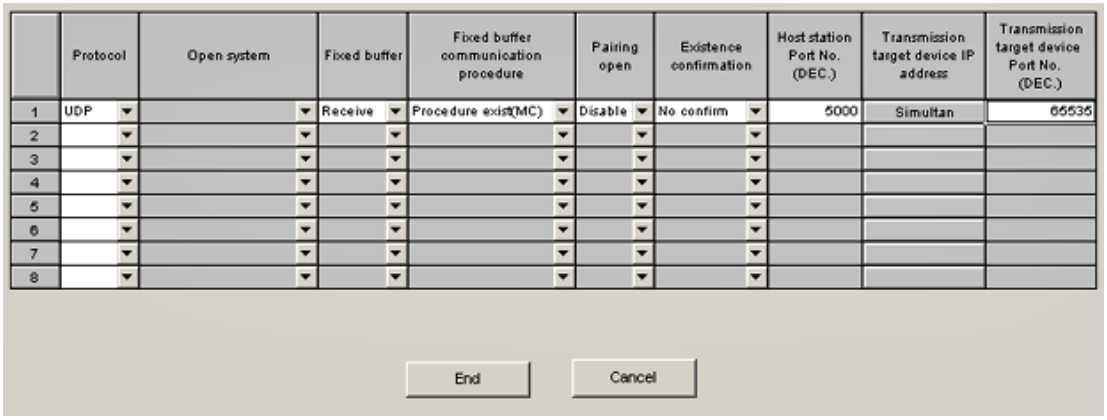
Note: The example shown below includes only one connection. To make multiple connections to the device from the OPC server, add another entry on this screen and configure another open port (such as, Port 5002). Check the device's manual to verify the device's available ports.



Open Settings for UDP

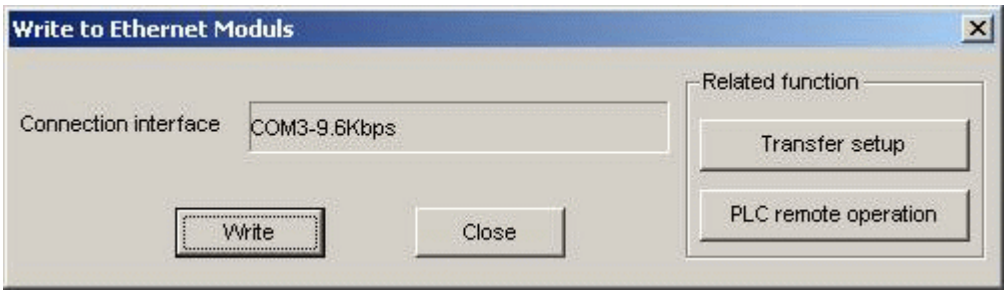
1. Enter **UDP** in the Protocol field. There are no open system options for UDP. The **Procedure exist (MC)** communications procedure sets the correct protocol in the FX3U-ENET block to communicate with this driver. In the example below, 5000 (1388 Hex) is specified in the Host station Port No. field.
2. To allow this driver to choose any port for communications, configure the target port as "unspecified" by entering 65535 (FFFFHex) in the Transmission target device Port No. field. The IP address that the driver uses can be specified or not. To enter the "unspecified" address of 255.255.255.255, do as shown below.





Write Network Parameters to PLC

After all of the network parameters have been specified, they must be written to the PLC. To do so, click **Write** from the main FX-Configurator-ENwindow.



Note: There must be a serial connection to the FX3U PLC. The configuration settings are written to the PLC via this serial link. Also make sure that the communication parameters are correct. Settings can be checked by clicking **Transfer Setup** or be selecting **Online** | **Transfer Setup** from the main menu.

Users must cycle the power on the PLC for the network parameter changes to take effect.

L Series PLC Setup

The following is provided for convenience only. Refer to the manufacturer's documentation for current and official instructions.

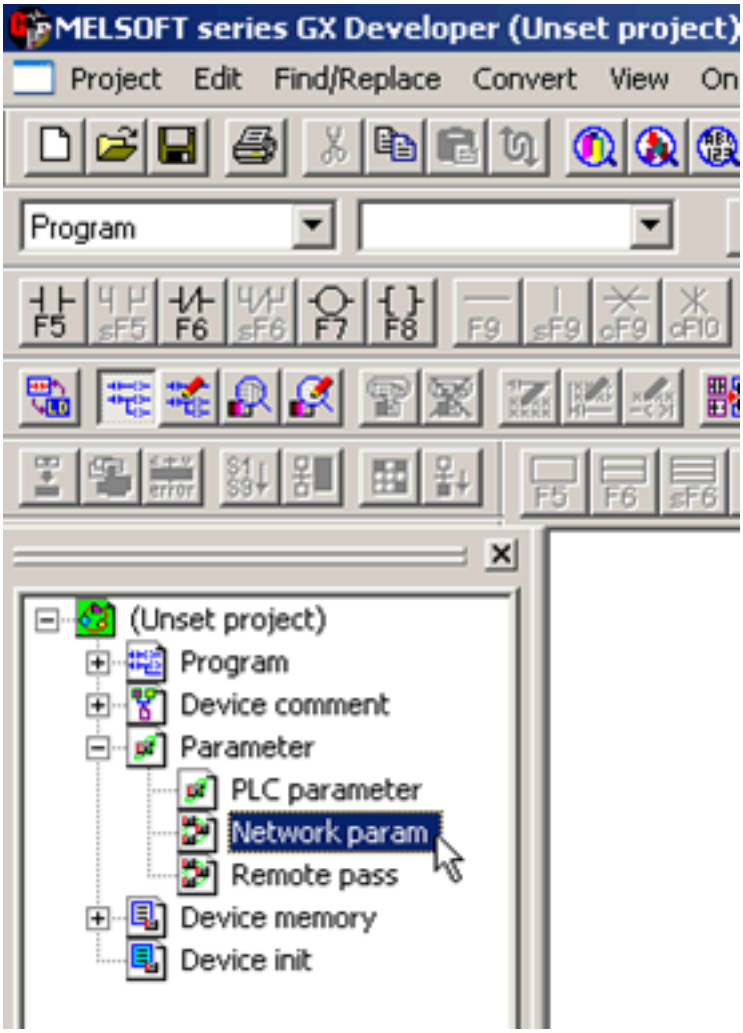
Like the Q series models, the newest L Series Ethernet modules (LJ71E71-100) do not have DIP switches that need to be set. Furthermore, special ladder logic to enable Ethernet communications is not required. Users must set network related parameters in the controller, however, using the Mitsubishi GX Developer software. Ports may be configured to use TCP/IP or UDP.

Note: TCP/IP is less efficient than UDP. Users planning to communicate with devices on a remote network should note that TCP/IP requires multiple ports be configured in the relay device. UDP is recommended wherever possible. *For more information, refer to* ***Multi-level Networks****.*

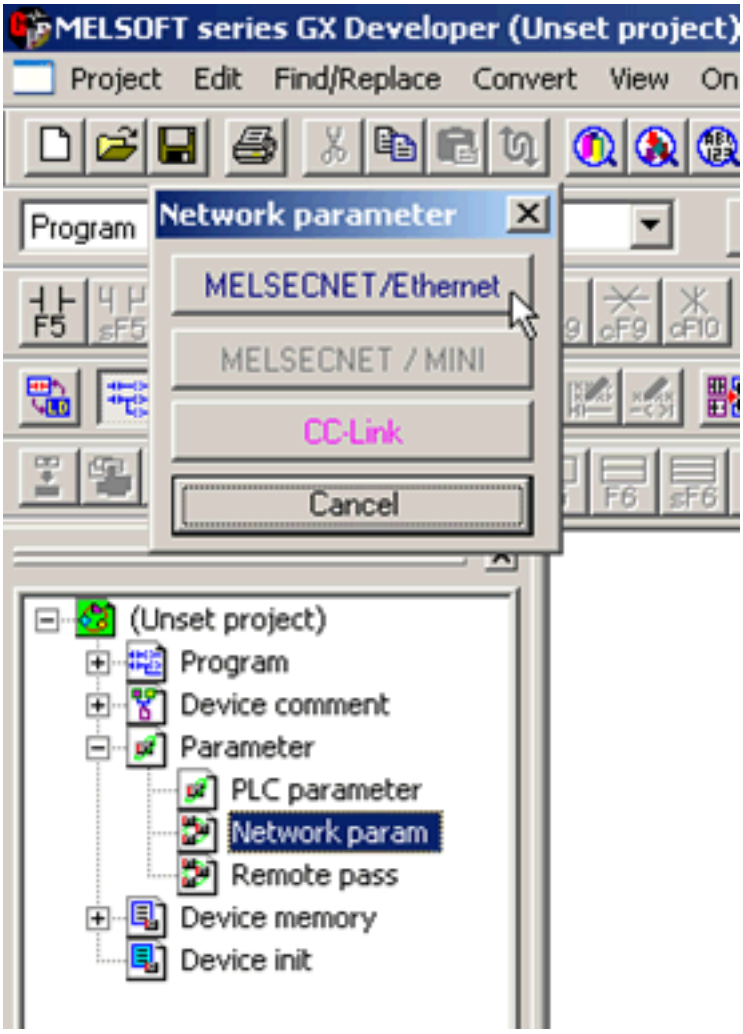
Device Configuration

1. To start, create a new GX Developer project for a L Series (LCPU) PLC. Alternatively, open and edit an existing project.

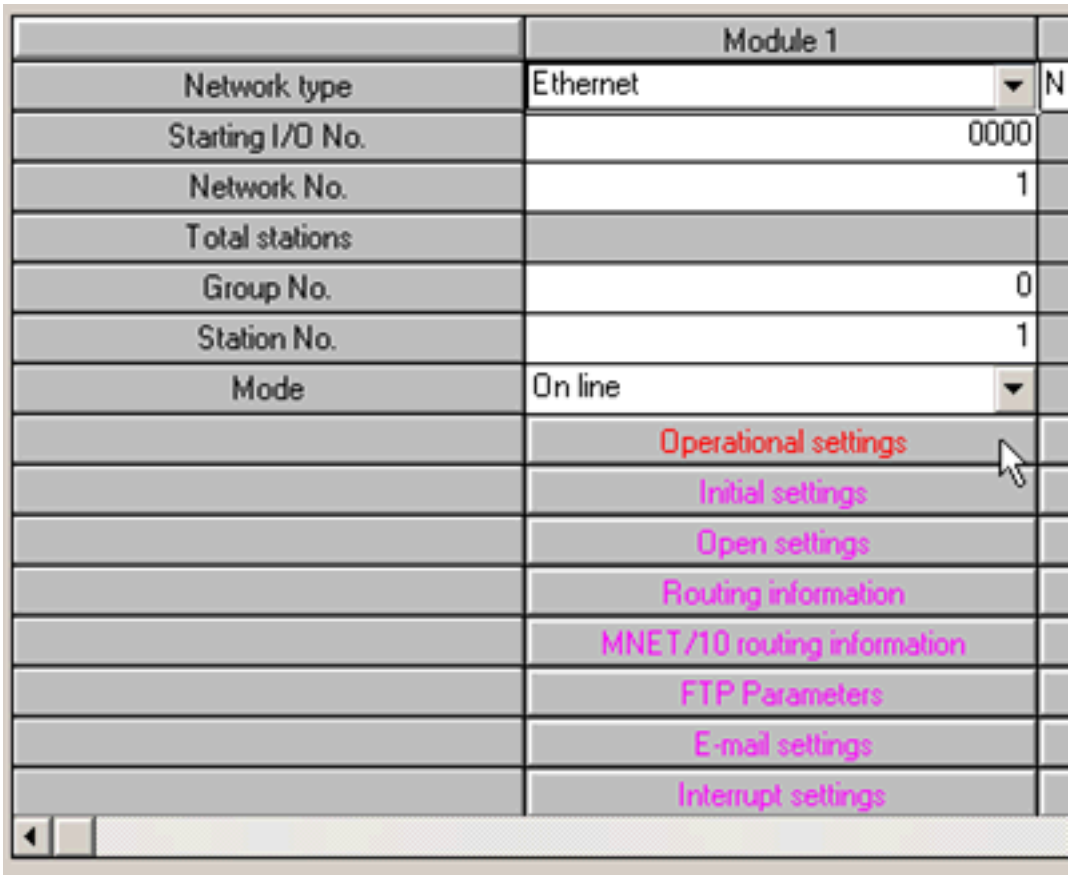
2. Next, select **Network Param**.



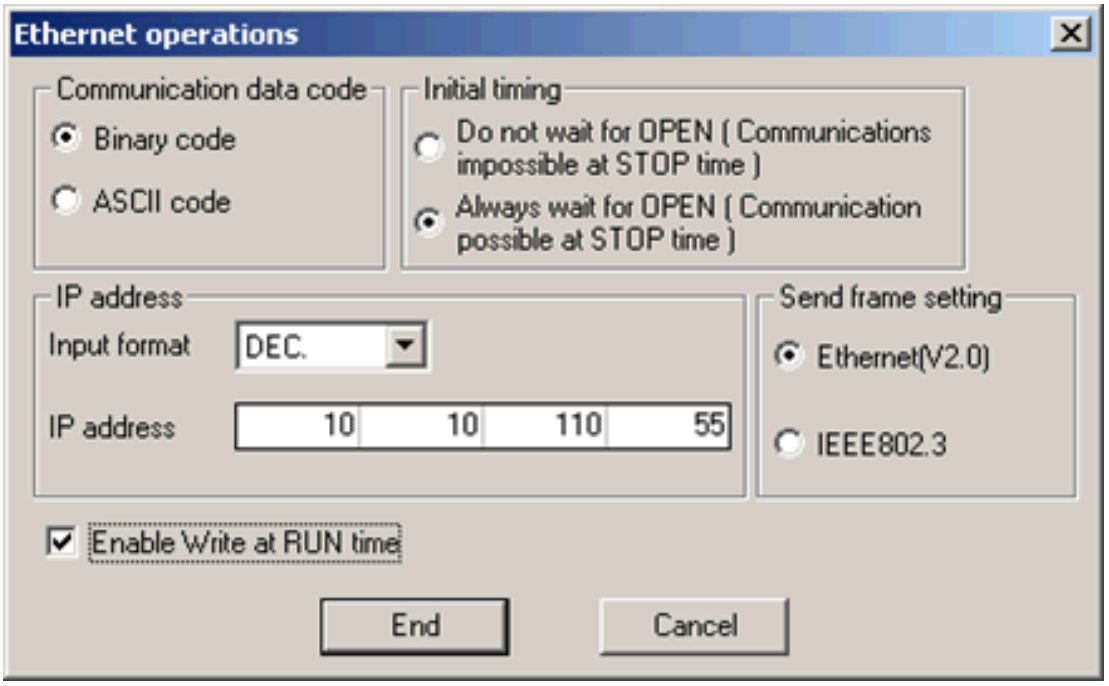
3. In Network Parameter, click **MELSECNET/Ethernet**.



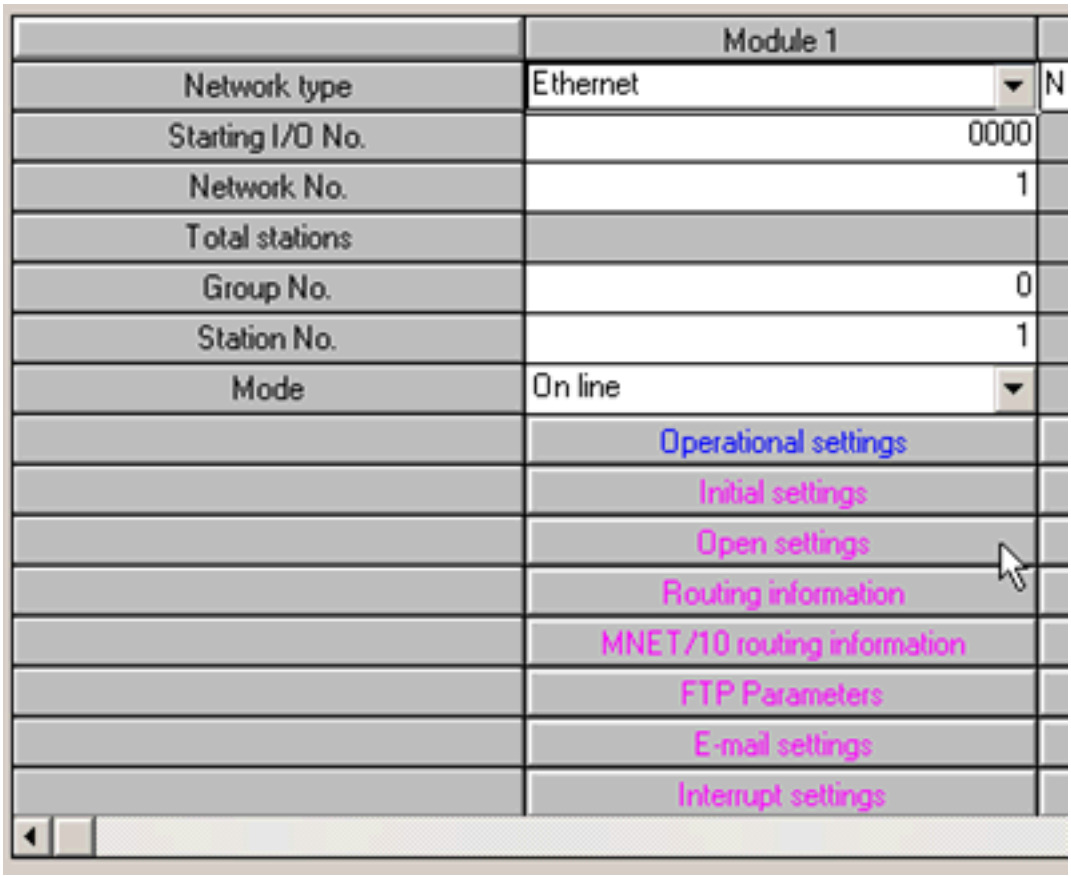
4. Fill in the required information for the Ethernet module. Although the network type must be Ethernet, other settings will depend on the particular application. The example below is for station 1 on network 1. The starting I/O No. is 0 in this case because the LJ71E71-100 Ethernet module is installed in the slot adjacent to the CPU. If there are other modules between the CPU and Ethernet unit, determine the total I/O mapped to those and set the starting I/O of the Ethernet unit accordingly. Once these basic network settings are specified, click on **Operational Settings**.



5. The **Ethernet Operations** dialog is used to define the device's IP address. Except for the IP address, the settings should be as shown below.
   Note: Unless security or safety concerns require otherwise, make sure **Enable Write at RUN time** is checked. If this is left unchecked, all writes will fail when the PLC is in Run mode.



6. Click **End**.
7. Upon returning to the basic network parameters dialog, click **Open settings**.



8. Specify the desired open settings. These depend on the chosen IP protocol, which may be TCP or UDP.

Open Settings for TCP

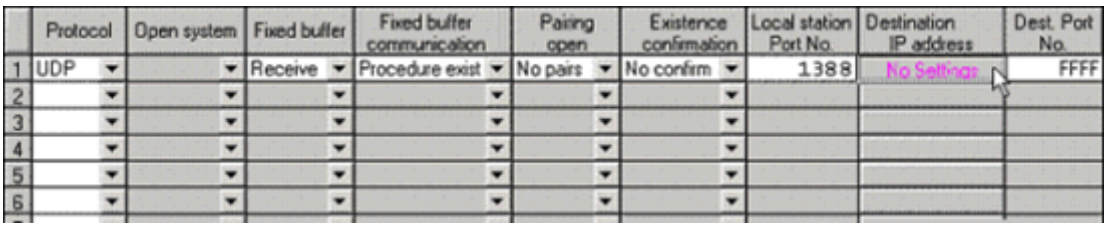
Enter **TCP** for the protocol. For simplicity, the **Unpassive** open system is recommended. By using the unpassive open system, users will not have to configure the IP and port that the driver will use. In the example below, the local port number 5001 (1389 Hex) is specified.

	Protocol	Open system	Fixed buffer	Fixed buffer communication	Pairing open	Existence confirmation	Local station Port No.	Destination IP address	Dest. Port No.
1	TCP	Unpassive	Send	Procedure exist	No pairs	No confirm	1389		
2									
3									
4									
5									
6									

Tip: Consult the Knowledge Base and the Mitsubishi Technical Bulletin "Existence Confirmation Configuration using Fixed Buffer Communications with a QJ71E71-100 Ethernet Module" for detailed information about device configuration for TCP communications.

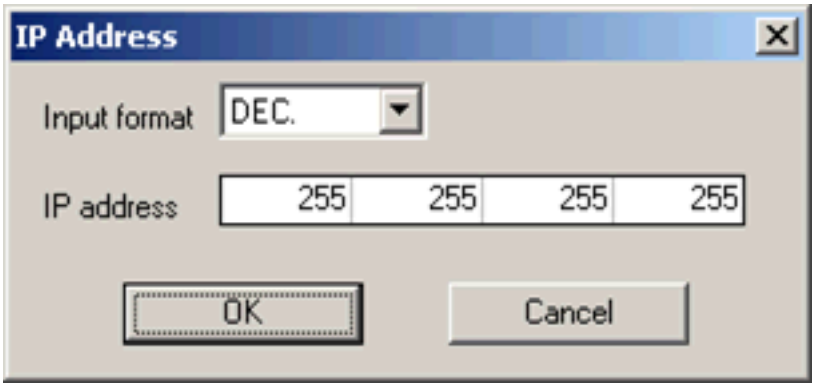
Open Settings for UDP

1. Enter **UDP** for the protocol. There are no open system options for UDP. In the example below, the local port number 5000 (1388 Hex) is specified.
2. Next, specify the destination IP and port. This would be the IP and port that the driver will use to communicate with the PLC. To prevent issues with conflicting port usage, the Mitsubishi Ethernet Driver allows Windows to assign any unused UDP port to each device configured in the driver on startup. Thus, the port that the driver will use is not predictable. Users must configure the destination port in the PLC as "unspecified". This is done by entering FFFF (Hex) as shown below.
3. Finally, click on the **Destination IP** address button.



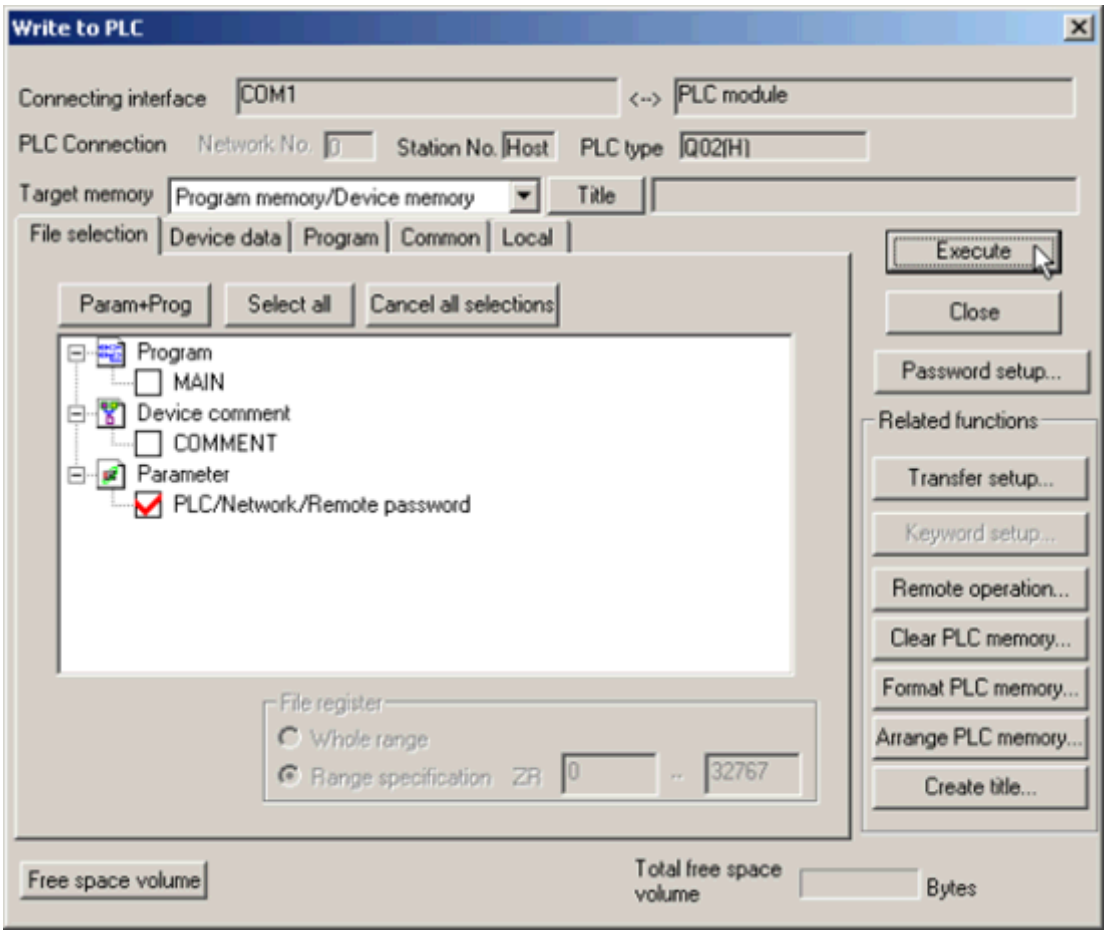
	Protocol	Open system	Fixed buffer	Fixed buffer communication	Pairing open	Existence confirmation	Local station Port No.	Destination IP address	Dest. Port No.
1	UDP		Receive	Procedure exist	No pairs	No confirm	1388	No Settings	FFFF
2									
3									
4									
5									
6									

4. Either specify the IP address that the driver will be using or leave it at the "unspecified" address of 255.255.255.255 as shown below.



Write Network Parameters to PLC

After all of the network parameters have been specified, they must be written to the PLC. This can be done by selecting the Online | Write To PLC... menu option. Check the network parameters file selection and then click **Execute**.



Note: Users must cycle the power on the PLC for the network parameter changes to take effect.

L Series Built-in Ethernet Port PLC Setup

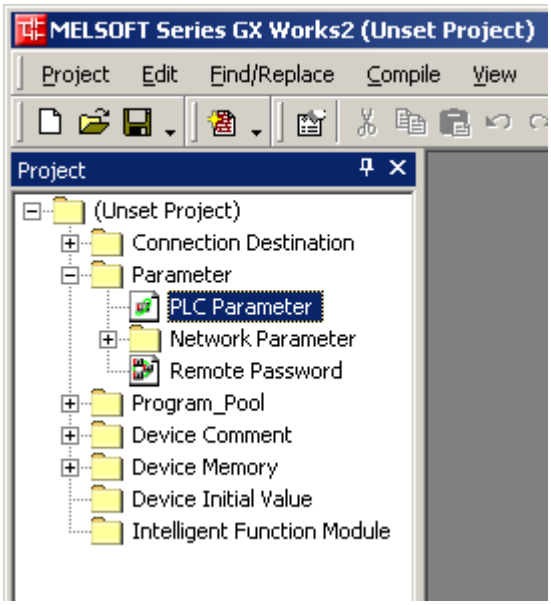
The following is provided for convenience only. Refer to the manufacturer's documentation for current and official instructions.

For the Mitsubishi Ethernet Driver to communicate with the Mitsubishi L Series CPU's built-in Ethernet port, some network parameters must be configured in the PLC.

Device Configuration

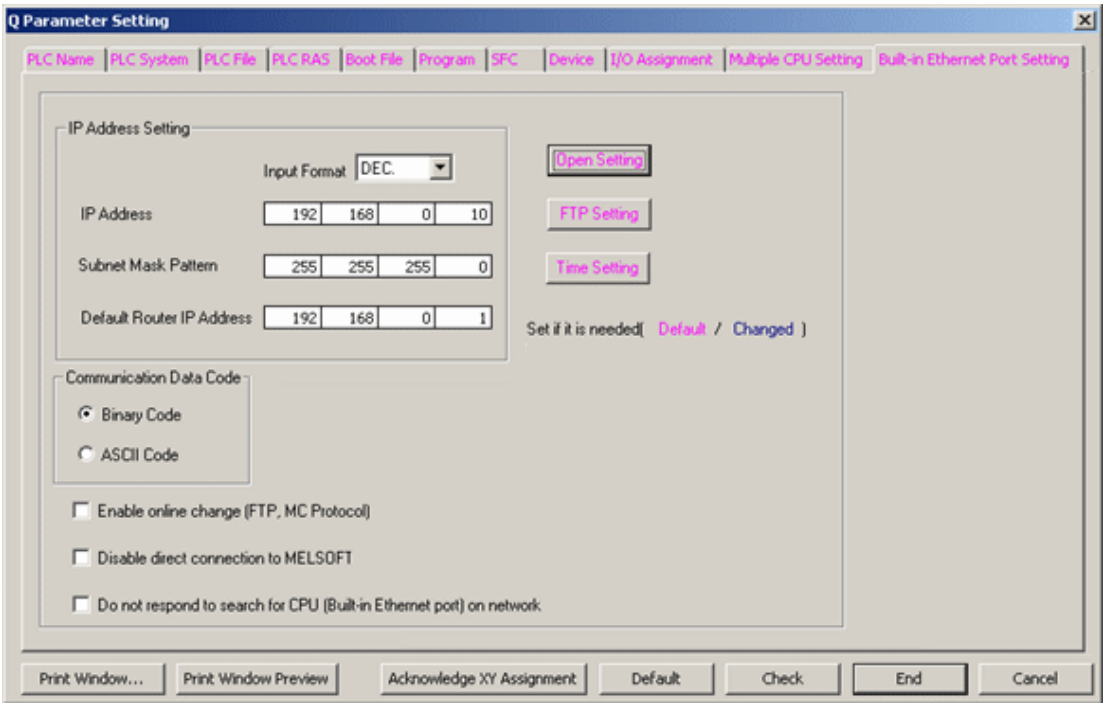
The following instructions were created using Mitsubishi GX Works2 software.

1. To start, create a new project for an L Series (LCPU) PLC. Alternatively, open and edit an existing project.
2. Next, select **PLC Parameter**.



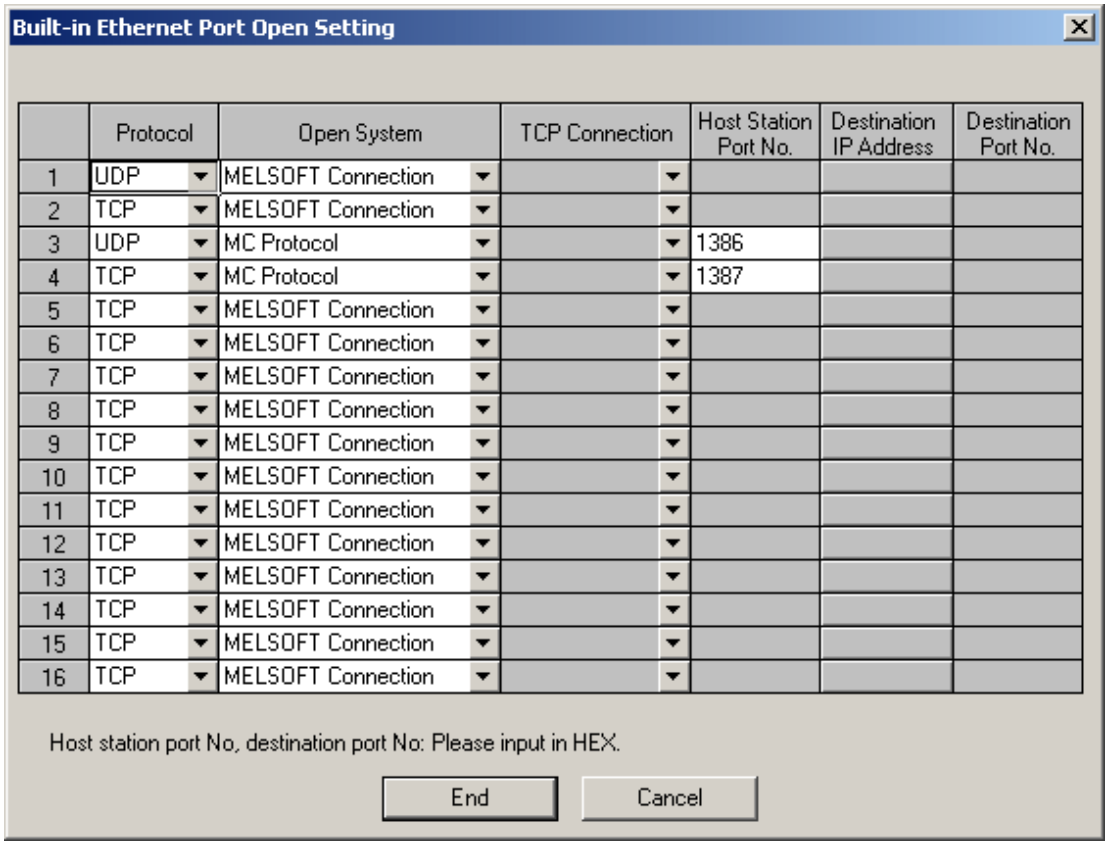
3. Open the **Built-in Ethernet Port Setting** tab, and then make the following changes:
    - Beneath **IP Address Setting**, fill in all required information.

- Beneath **Communication Data Code**, select **Binary Code**.



4. Next, click **Open Setting**, and then make the following changes:

   - Specify the **Protocol**. Options include **UDP** or **TCP**.
   - Specify the **Open System** as **MC Protocol**.
   - Specify the **Host Station Port No**.



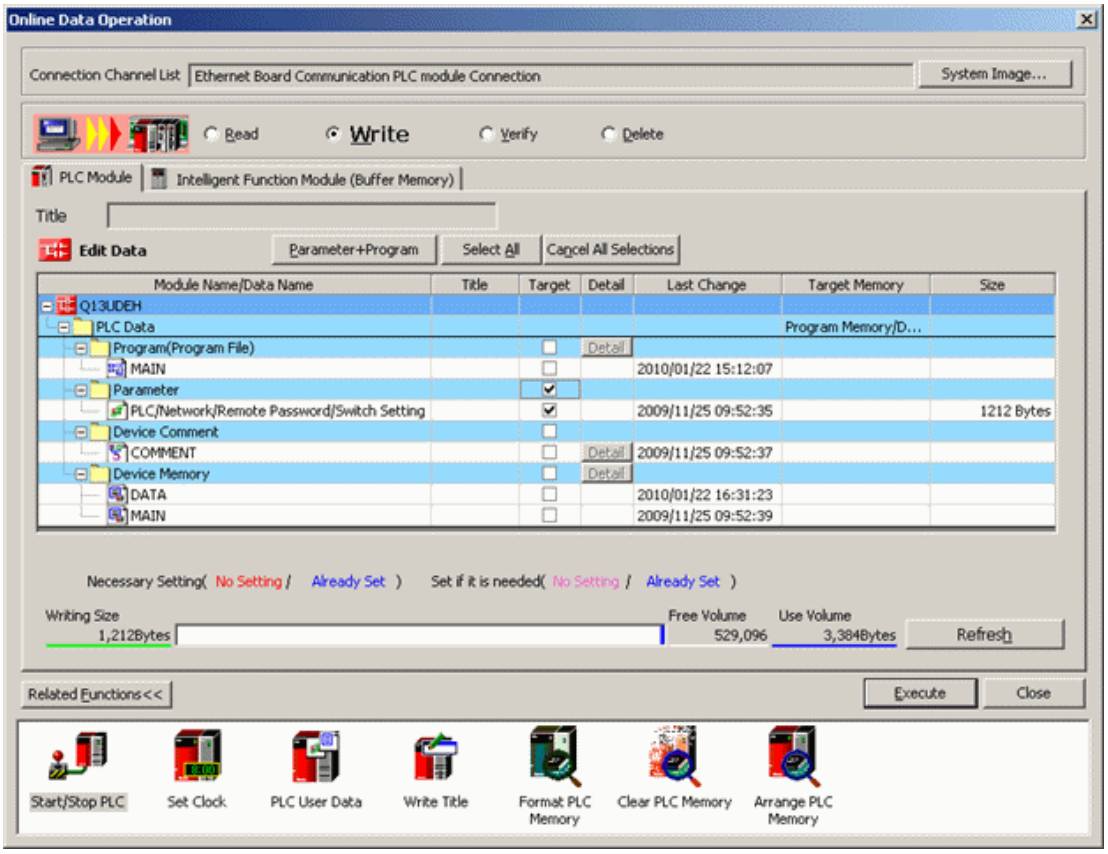
Note: In the example above, the local port numbers 4998 (1386H) and 4999 (1387H) are used.

Important: The driver's default port settings of 5000 UDP and 5001 TCP are not valid port numbers for the built-in Ethernet port. The driver uses decimal numbers for the port number; GX Works2 uses hexadecimal number for the port numbers. Valid port number setting ranges are 0401H (1025) to 1387H (4999), and 1392H (5010) to FFFEH (65534).

5. Click **End**.

Writing the Network Parameters to the PLC

After all network parameters have been specified, they must be written to the PLC. To do so, click **Online** | **Write To PLC...**. Then, check **Parameter** (located beneath **Target**) and then click **Execute**.



Note: Users must cycle the power on the PLC for the network parameter changes to take effect.

QnA Series PLC Setup

The following is provided for convenience only. Refer to the manufacturer's documentation for current and official instructions.

Hardware Settings

The DIP switches on the A1SJ71QE71 Ethernet interface card must be set as follows:

- DIP switches 1-2 must be set to OFF.
- DIP switch 3 must be set to ON.
- DIP switches 4-6 must be set to OFF.
- DIP switch 7 must be set to ON.
- DIP switch 8 must be set to OFF.

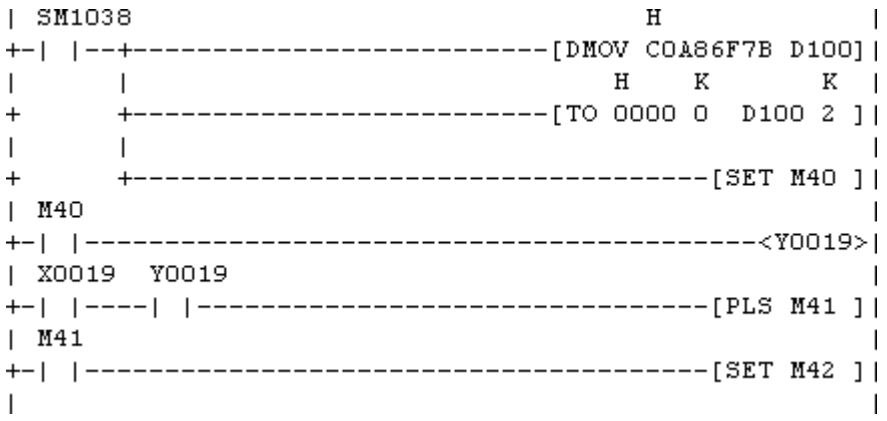
Ladder Program

The Mitsubishi QnA Series PLC requires that a ladder program be used to initialize the AJ71QE71 or A1SJ71QE71 Ethernet interface card and define the desired open system. TCP/IP and UDP open systems may be used with this driver. In the case of TCP/IP, error handling code should also be implemented. Note that TCP/IP is less efficient than UDP and requires a special ladder to handle network error recovery. Also, if planning to communicate with devices on a remote network, TCP/IP requires that multiple ports be configured in the relay device. Thus, UDP is recommended wherever possible. For more information, refer to **Multi-level Networks**.

Note: Power must be cycled to the PLC for any network configuration to take effect.

Initialization Ladder

The following initialization code sets the IP address of the device and triggers execution of the open code. For this example, an IP of 192.168.111.123 (C0.A8.6F.7B Hex) is assumed.

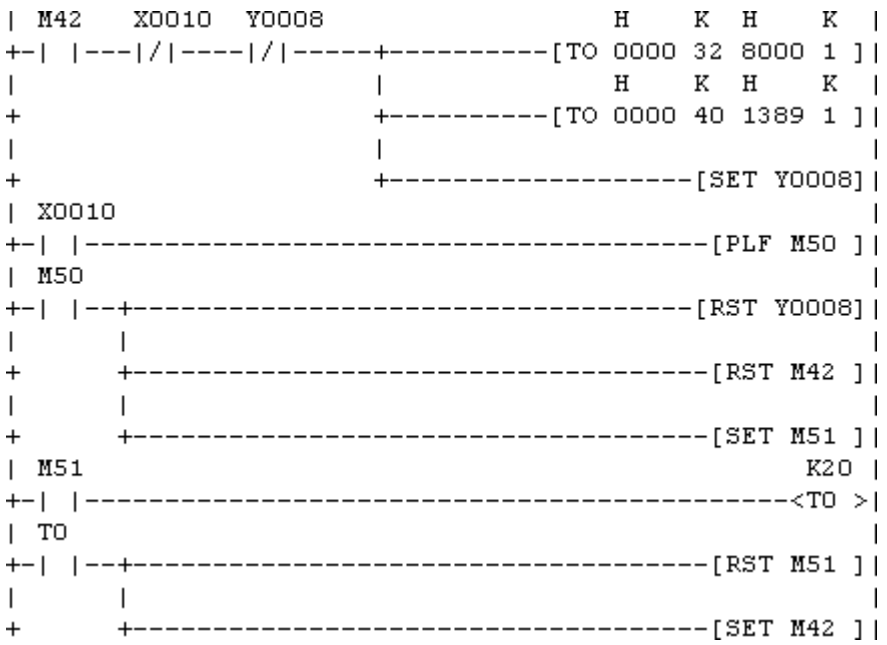


Open and Error Handling Ladder for TCP/IP

The following open and error handling code assumes TCP/IP communications, unpassive mode, on port 5001 (1389 Hex).

This code is for the first communications buffer of the A1SJ71QE71 card. Similar code must be implemented for each addition buffer needed. Ensure that the proper interface bits are used as well as separate error handling bits and timers for each buffer.

Note: It is strongly recommended that users follow the code fragment as closely as possible. Without proper error handling and recovery on the PLC side of the connection, communications may not able to be reestablished with the PLC after a physical error, such as a cable break, occurs. Without the error handling represented here, the PLC may need to be reset to reestablish communications.



Given the ladder fragment shown here for TCP/IP port operation, the A1SJ71QE71 will be forced to close and re-enable the port for a connection if the current connection is lost. This will occur 2 seconds after the error

is detected as controlled by T0. Reloading the port mode and port number and the set of Y008 resets the port.

Open Ladder for UDP

The following open code assumes UDP communications on port 5000 (1388 Hex). The UDP open system requires that the destination address be specified. This would be the IP and port that the driver will use to communicate with the PLC. To prevent issues with conflicting port usage, the Mitsubishi Ethernet Driver allows Windows to assign any unused UDP port to each device configured in the driver on startup. Thus, the port that the driver will use is not predictable. Users must configure the destination port in the PLC as "unspecified". This is done by entering FFFF (Hex) as shown below. The exact IP address the driver will use may be specified. This example assumes 192.168.111.24 (C0.A8.6F.18 Hex). However, the destination may also be left as "unspecified" with 255.255.255.255 (FF.FF.FF.FF Hex).

Note: If a specific IP address is put into the ladder code, only the machine with that IP address will be able to communicate with the PLC via UDP. If the IP address is left as "unspecified," then any IP address can communicate with the PLC.

```
| M42   X0010  Y0008                  H    K  H    K  |
+-| |---|/|----|/|-----+----------[TO 0000 32 110  1 ]|
|                       |             H    K  H    K  |
+                       +----------[TO 0000 40 1388 1 ]|
|                       |             H    K  H    K  |
+                       +----------[TO 0000 41 6F18 1 ]|
|                       |             H    K  H    K  |
+                       +----------[TO 0000 42 C0A8 1 ]|
|                       |             H    K  H    K  |
+                       +----------[TO 0000 43 FFFF 1 ]|
|                       |                             |
+                       +-------------------[SET Y0008]|
```

Q Series PLC Setup

The following is provided for convenience only. Refer to the manufacturer's documentation for current and official instructions.

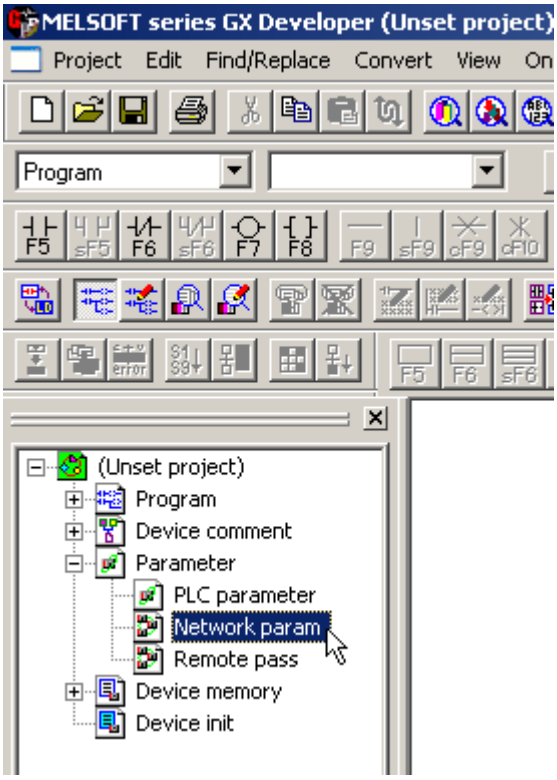
Unlike the A and QnA series, the newest Q Series Ethernet modules (QJ71E71-100) do not have DIP switches that need to be set. Furthermore, special ladder logic to enable Ethernet communications is not required. Users must set network related parameters in the controller, however, using the Mitsubishi GX Developer software. Ports may be configured to use TCP/IP or UDP.

Note: TCP/IP is less efficient than UDP. Users planning to communicate with devices on a remote network should note that TCP/IP requires multiple ports be configured in the relay device. UDP is recommended wherever possible. For more information, refer to **Multi-level Networks**.

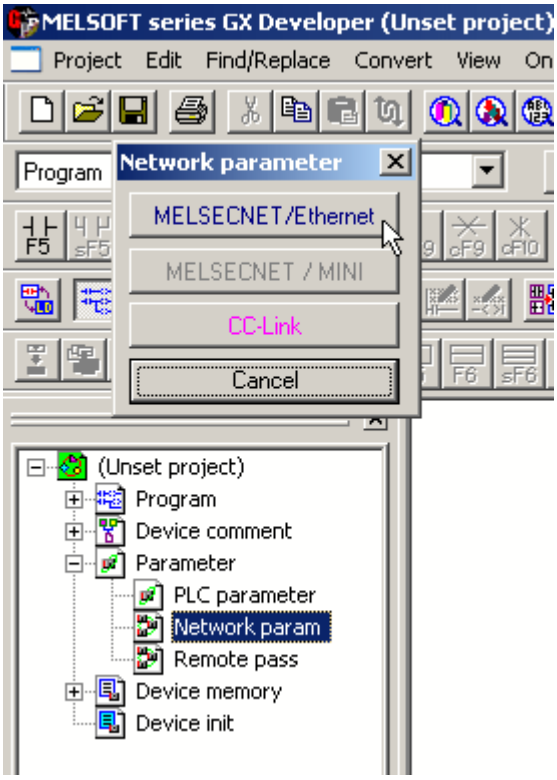
Device Configuration

1. To start, create a new GX Developer project for a Q Series (Q mode) PLC. Alternatively, open and edit an existing project.

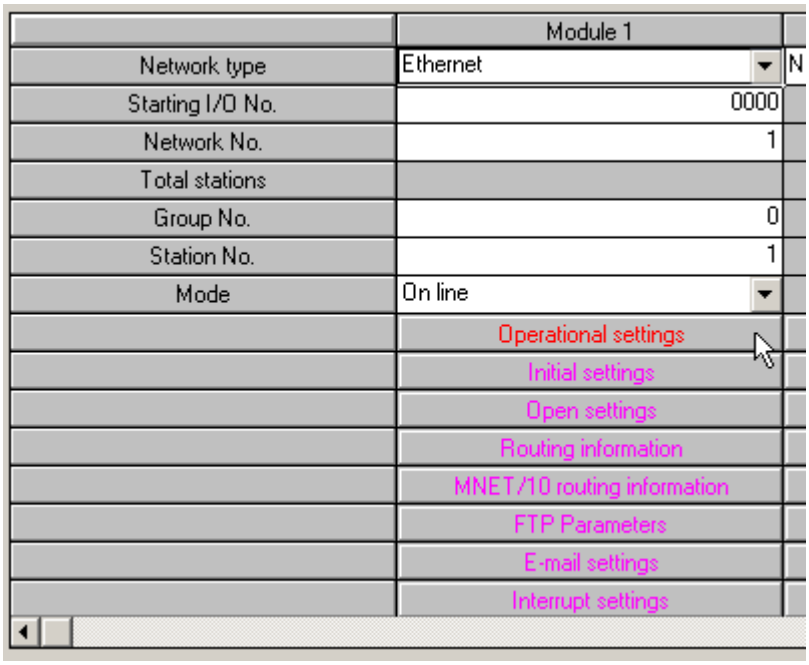
2. Next, select **Network Param**.



3. In **Network Parameter**, click **MELSECNET/Ethernet**.

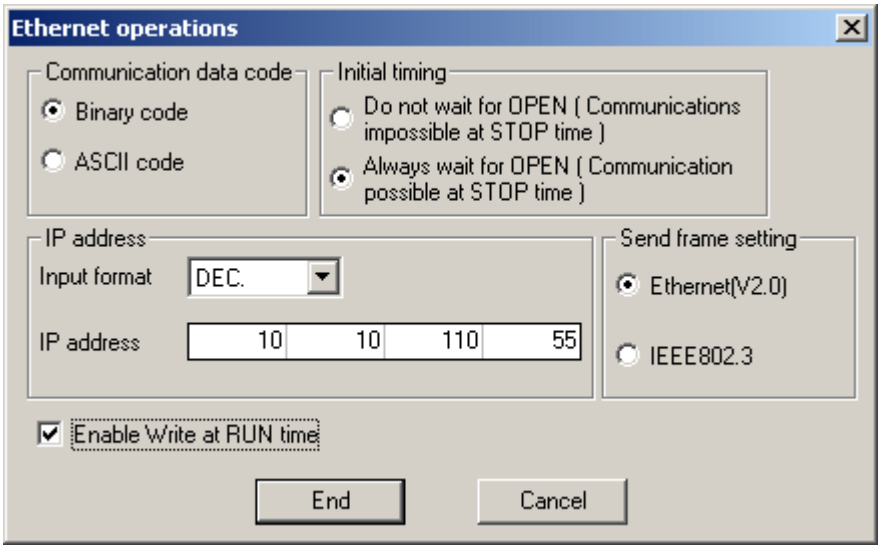


4. Fill in the required information for the Ethernet module. Although the network type must be Ethernet, other settings will depend on the particular application. The example below is for station 1 on network 1. The starting I/O No. is 0 in this case because the QJ71E71 Ethernet module is installed in the slot adjacent to the CPU. If there are other modules between the CPU and Ethernet unit, determine the total I/O mapped to those and set the starting I/O of the Ethernet unit accordingly. Once these basic network settings are specified, click on **Operational Settings**.



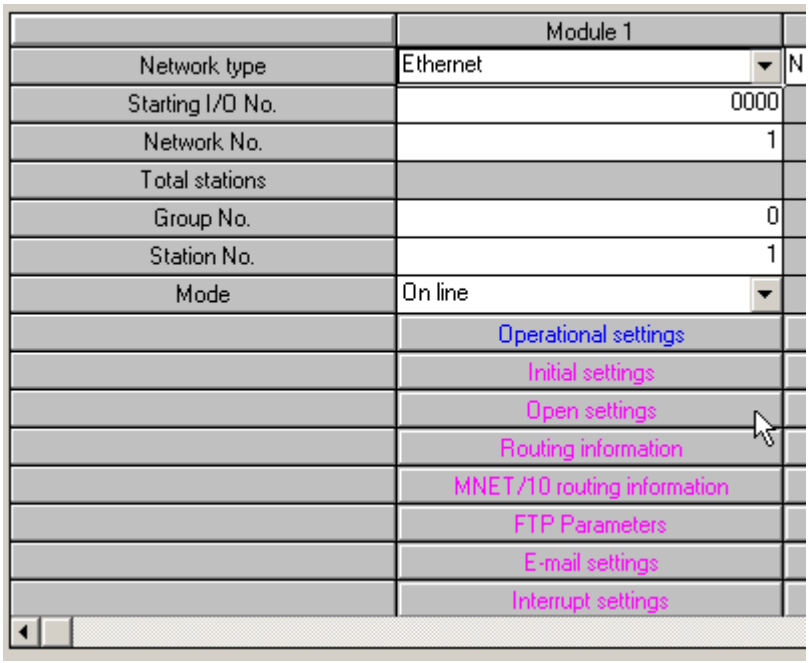
5. The **Ethernet Operations** dialog is used to define the device's IP address. Except for the IP address, the settings should be as shown below.

Note: Unless security or safety concerns require otherwise, make sure "Enable Write at RUN time" is checked. If this is left unchecked, all writes will fail when the PLC is in Run mode.



6. Click **End**.

7. Upon returning to the basic network parameters dialog, click **Open settings**.



8. Specify the desired open settings. These depend on the chosen IP protocol, which may be TCP or UDP.

Open Settings for TCP

Enter **TCP** for the protocol. For simplicity, the **Unpassive** open system is recommended. By using the unpassive open system, users will not have to configure the IP and port that the driver will use. In the example below, the local port number 5001 (1389 Hex) is specified.

	Protocol	Open system	Fixed buffer	Fixed buffer communication	Pairing open	Existence confirmation	Local station Port No.	Destination IP address	Dest. Port No.
1	TCP	Unpassive	Send	Procedure exist	No pairs	No confirm	1389		
2									
3									
4									
5									
6									

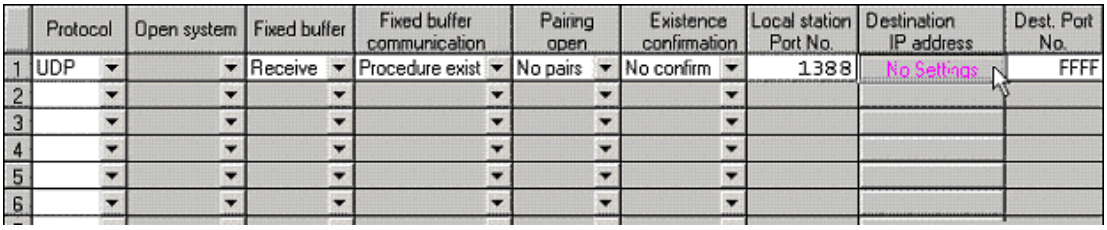
Tip: Consult the Knowledge Base and the Mitsubishi Technical Bulletin "Existence Confirmation Configuration using Fixed Buffer Communications with a QJ71E71-100 Ethernet Module" for detailed information about device configuration for TCP communications.

Open Settings for UDP

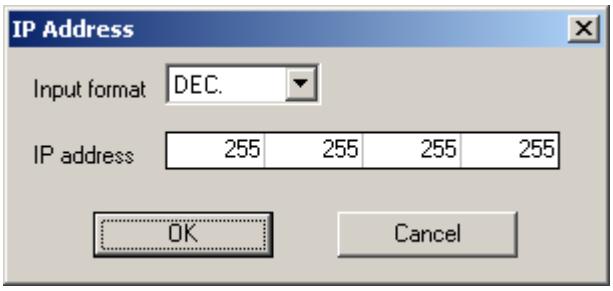
1. Enter **UDP** for the protocol. There are no open system options for UDP. In the example below, the local port number 5000 (1388 Hex) is specified.

2. Next, specify the destination IP and port. This would be the IP and port that the driver will use to communicate with the PLC. To prevent issues with conflicting port usage, the Mitsubishi Ethernet Driver allows Windows to assign any unused UDP port to each device configured in the driver on startup. Thus, the port that the driver will use is not predictable. Users must configure the destination port in the PLC as "unspecified". This is done by entering FFFF (Hex) as shown below.

3. Finally, click on the Destination IP address button.

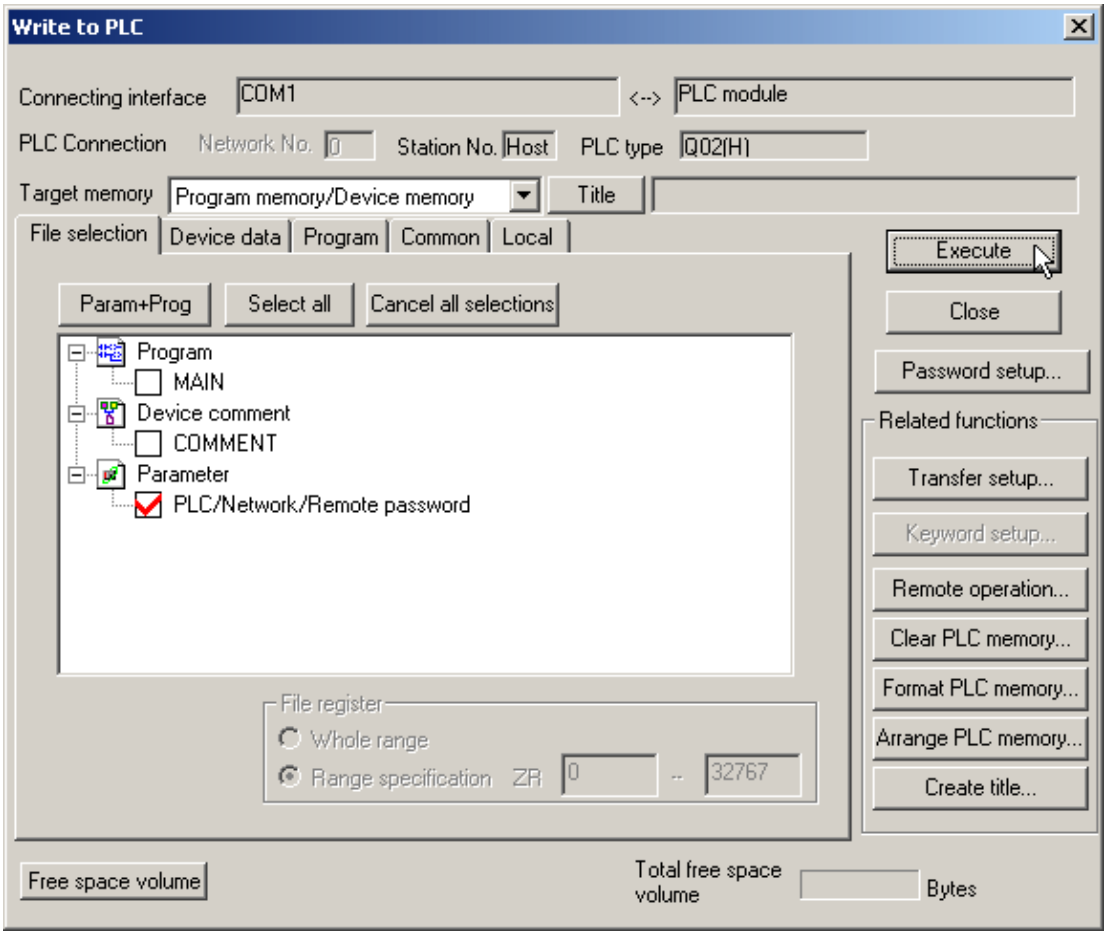


4. Either specify the IP address that the driver will be using or leave it at the "unspecified" address of 255.255.255.255 as shown below.



Write Network Parameters to PLC

After all of the network parameters have been specified, they must be written to the PLC. This can be done by selecting the **Online | Write To PLC...** menu option. Check the network parameters file selection and then click **Execute**.



Note: Users must cycle the power on the PLC for the network parameter changes to take effect.

iQ-R Series PLC Setup

The following is provided for convenience only. Refer to the manufacturer's documentation for current and official instructions.

Like the Q series models, the newest iQ-R Series Ethernet modules (R08 CPU) do not have DIP switches that need to be set. Furthermore, special ladder logic to enable Ethernet communications is required. Users must set network related parameters in the controller via the Mitsubishi GX Developer software. Ports may be configured to use TCP/IP or UDP.

Note: TCP/IP is less efficient than UDP. Users planning to communicate with devices on a remote network should note that TCP/IP requires multiple ports be configured in the relay device. UDP is recommended wherever possible. For more information, refer to **Multi-level Networks**.

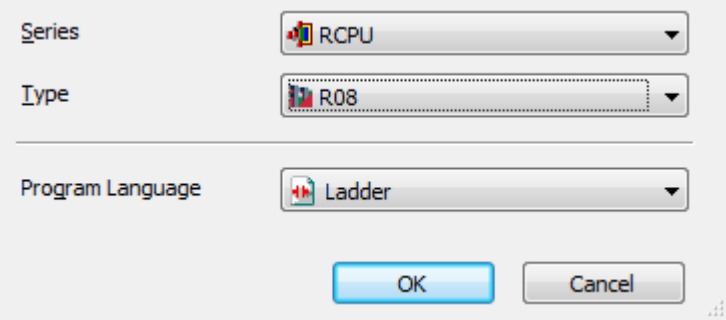
Device Configuration

1. To start, create a new GX Developer project for a iQ-R Series (R08) PLC. Alternatively, open and edit an existing project.

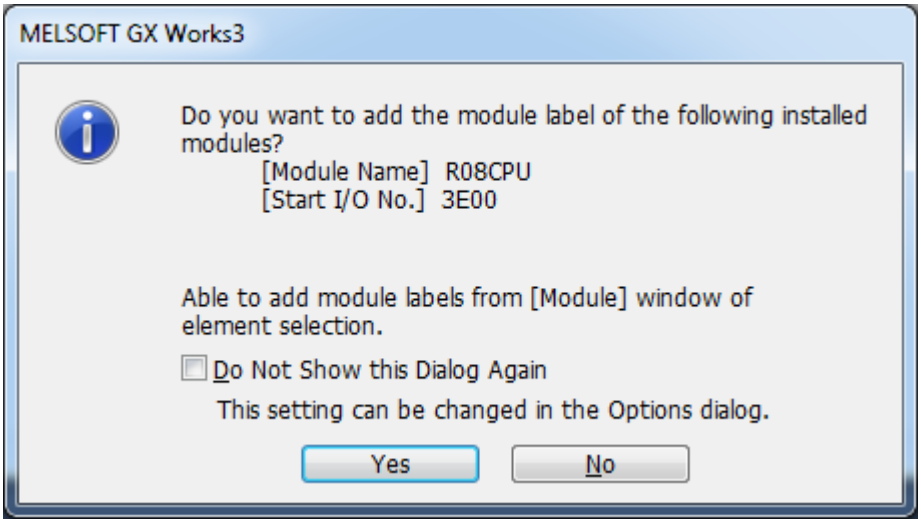
   Note: Use GX Works3 for iQ-F and iQ-R series.

2. Connect GX Works3 to the CPU module and set the parameters.
3. Set the CPU module as follows:

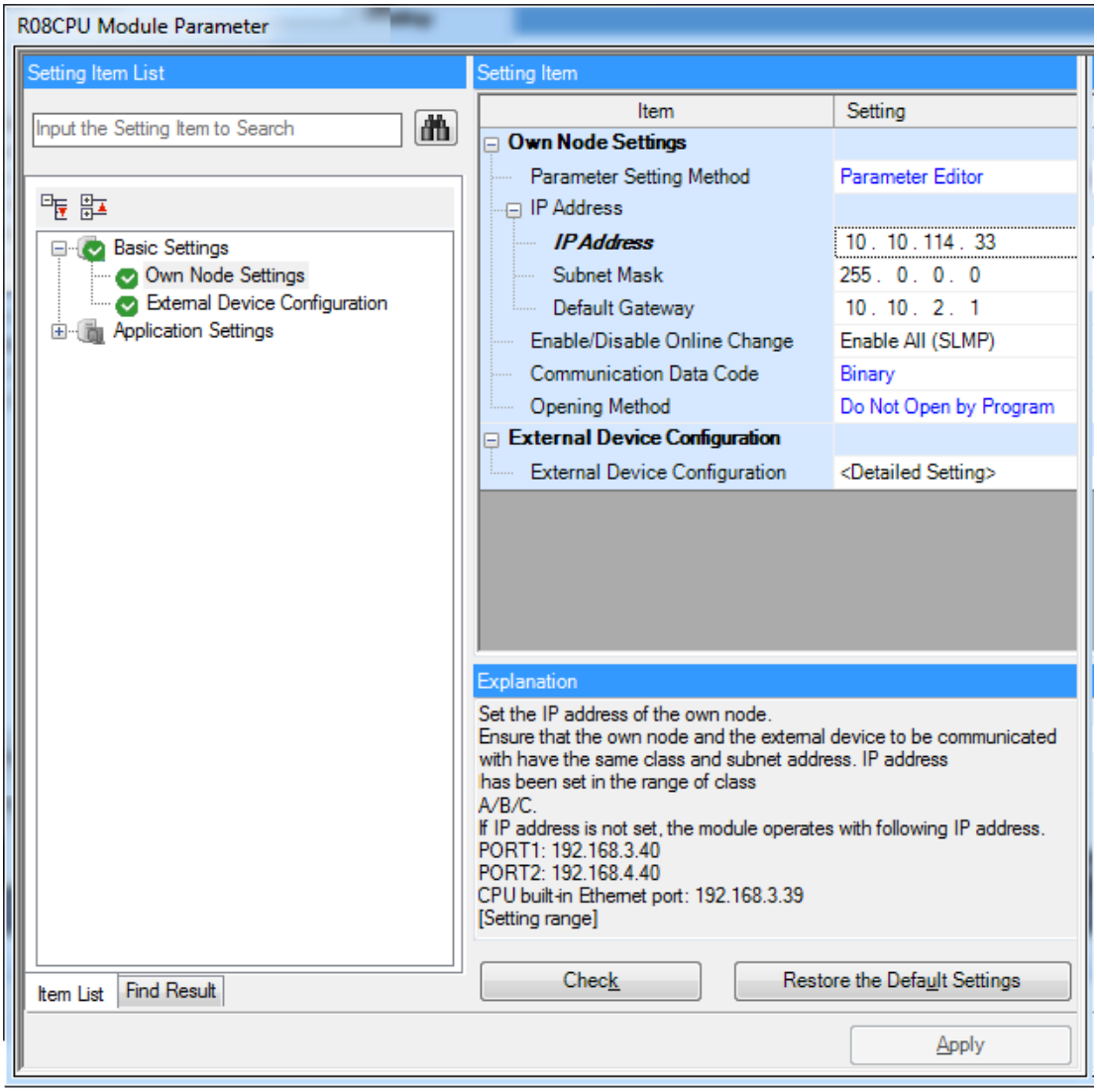
    - **Series:** RCPU
    - **Type:** R08
    - **Program Language:** Ladder



4. Click **Yes** to add the module labels of the CPU module.

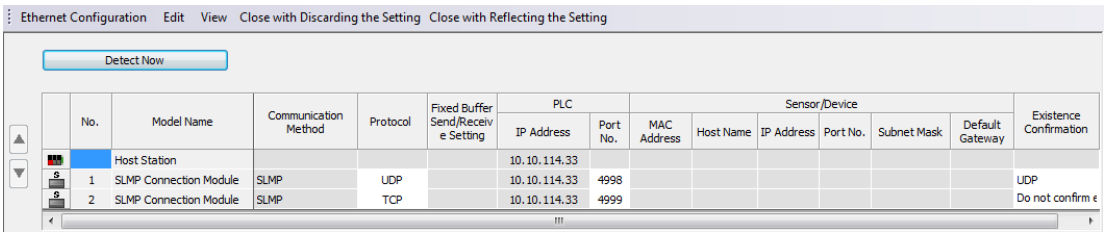


5. Set the IP Address settings by selecting **Basic Settings** in the Navigation window under **Parameter** | **R08CPU** | **Module Parameter** | **Ethernet Port**.



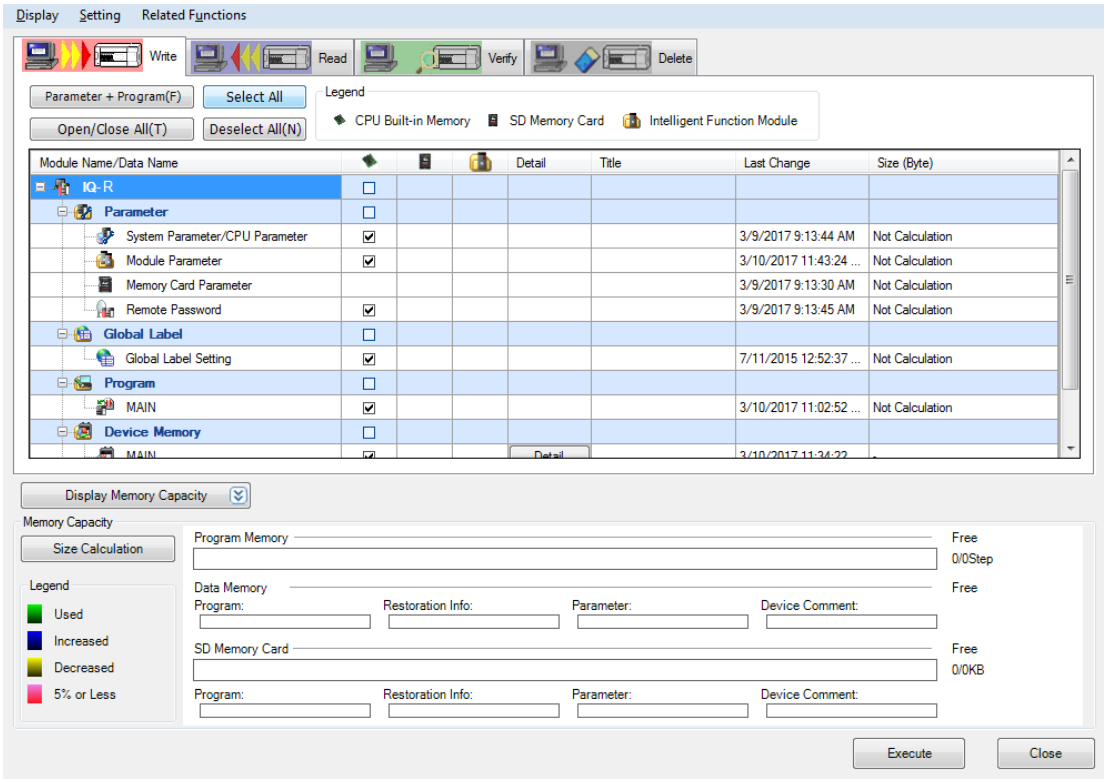
6. Set the **External Device Configuration** in the Navigation window under **Parameter** | **R08CPU** | **Module Parameter** | **Ethernet Port** | **Basic Settings**.

7. Specify the desired open settings. These depend on the chosen IP protocol, which may be TCP or UDP.



Write Network Parameters to PLC

After all of the network parameters have been specified, they must be written to the PLC. Select **Online** | **Write To PLC...** to specify the network parameters.



Note: Users must cycle the power on the PLC for the network parameter changes to take effect.

iQ-F Series PLC Setup

The following is provided for convenience only. Refer to the manufacturer's documentation for current and official instructions.

Like the Q series models, the newest iQ-F Series Ethernet modules (FX5U-32M) do not have DIP switches that need to be set. Furthermore, special ladder logic to enable Ethernet communications is required. Users must set network related parameters in the controller via the Mitsubishi GX Developer software. Ports may be configured to use TCP/IP or UDP.

Note: TCP/IP is less efficient than UDP. Users planning to communicate with devices on a remote network should note that TCP/IP requires multiple ports be configured in the relay device. UDP is recommended wherever possible. For more information, refer to **Multi-level Networks**.

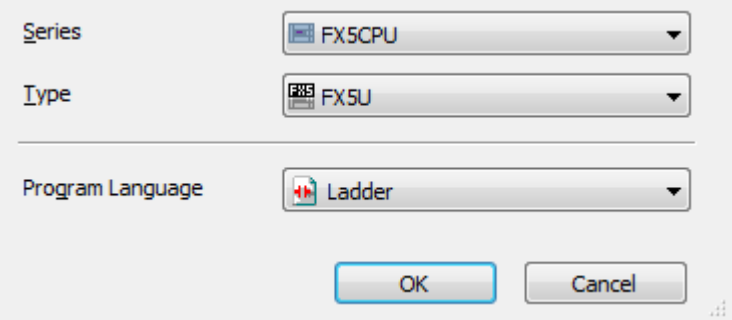
Device Configuration

1. To start, create a new GX Developer project for a iQ-F Series (FX5) PLC. Alternatively, open and edit an existing project.

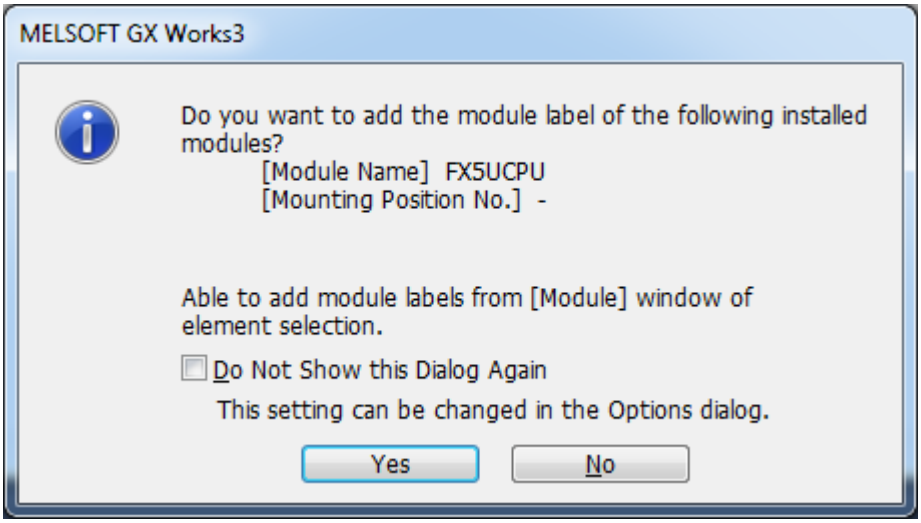
   Note: Use GX Works3 for iQ-F and iQ-R series.

2. Connect GX Works3 to the CPU module and set the parameters.
3. Set the CPU module as follows:

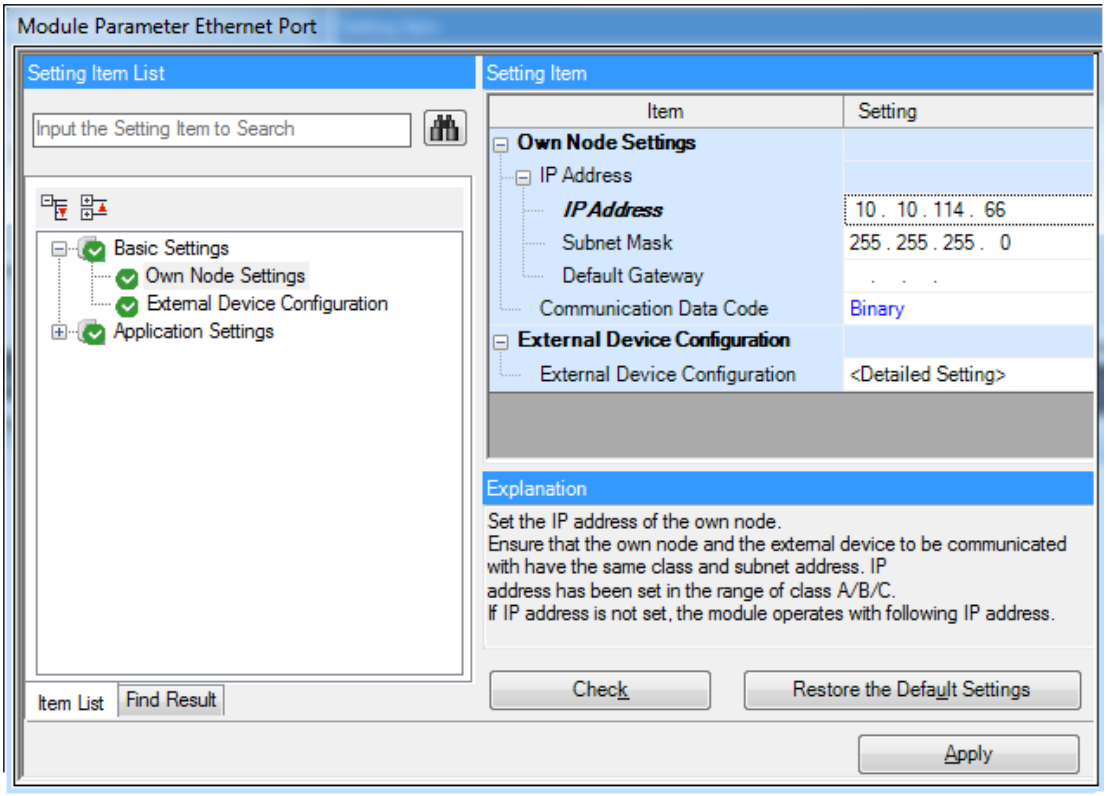
- **Series:** FX5CPU
- **Type:** FX5U
- **Program Language:** Ladder



4. Click **Yes** to add the module labels of the CPU module.

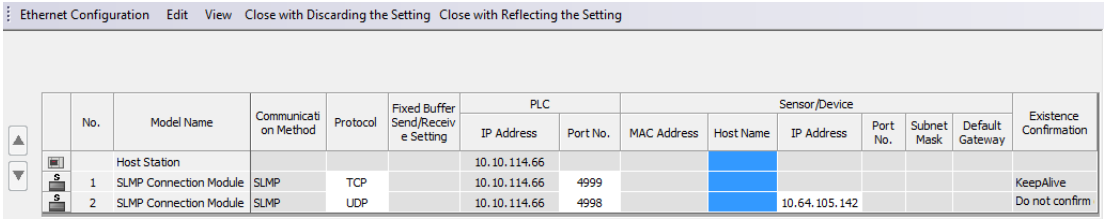


5. Set the IP Address settings by selecting **Basic Settings** in the Navigation window under **Parameter** | **FX5UCPU** | **Module Parameter** | **Ethernet Port**.



6. Set the **External Device Configuration** in the Navigation window under **Parameter** | **FX5UCPU** | **Module Parameter** | **Ethernet Port** | **Basic Settings**.

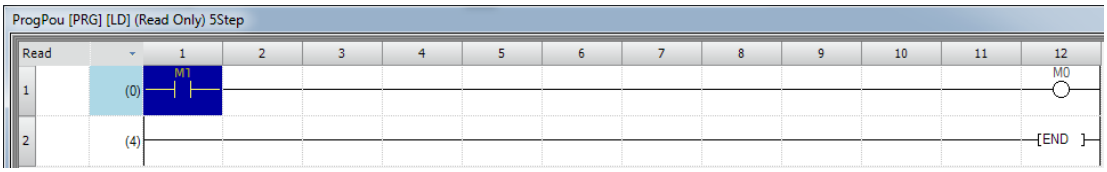
7. Specify the desired open settings. These depend on the chosen IP protocol, which may be TCP or UDP.



Setting up the Ladder Program

1. Create a new ladder logic file by accessing **Program** | **Scan** | **Main** | **ProgPou** | **Program** in the Navigation window.

Note: For UDP Protocol Sensor/Device IP Address, the IP of the machine where the server is running must be specified.

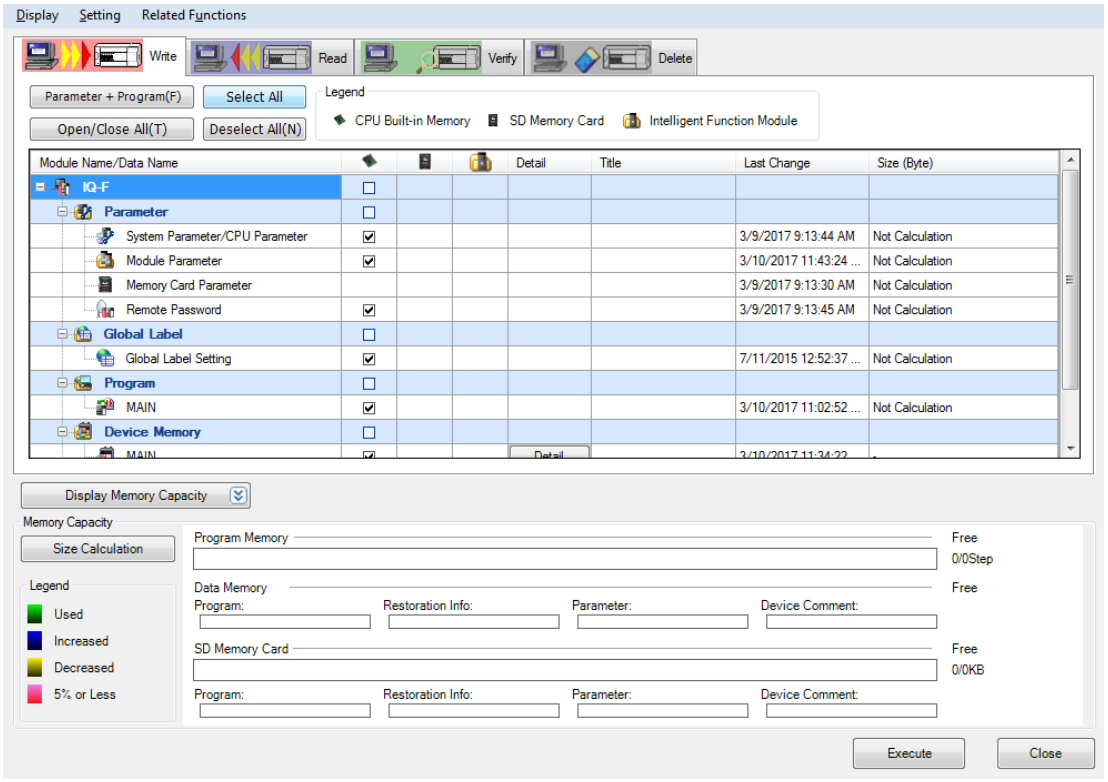


2. To convert the ladder program, select the **Convert** from the main window menu options and click **Convert**.

Note: When the ladder program is incorrect, text in the Navigation window is red. Once successfully configured, the text returns to the default color (white).

Write Network Parameters to PLC

After all of the network parameters have been specified, they must be written to the PLC. Select **Online | Write To PLC...** to specify the network parameters.



Note: Users must cycle the power on the PLC for the network parameter changes to take effect.

Q Series Built-in Ethernet Port PLC Setup

The following is provided for convenience only. Refer to the manufacturer's documentation for current and official instructions.

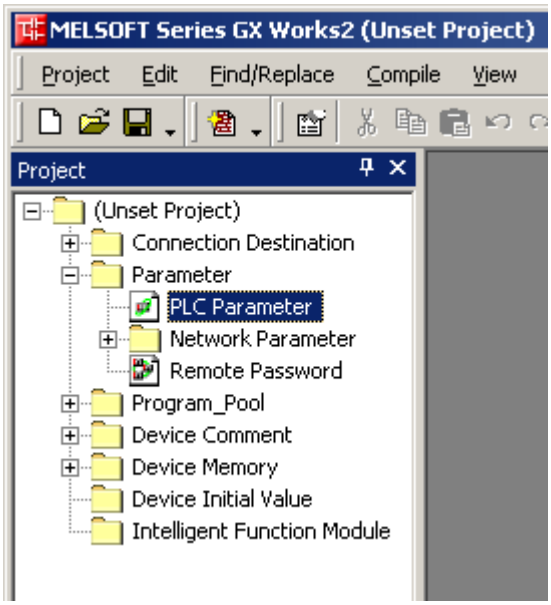
For the Mitsubishi Ethernet Driver to communicate with the Mitsubishi Q Series CPU's built-in Ethernet port, some network parameters must be configured in the PLC.

Device Configuration

The following instructions were created using Mitsubishi GX Works2 software.

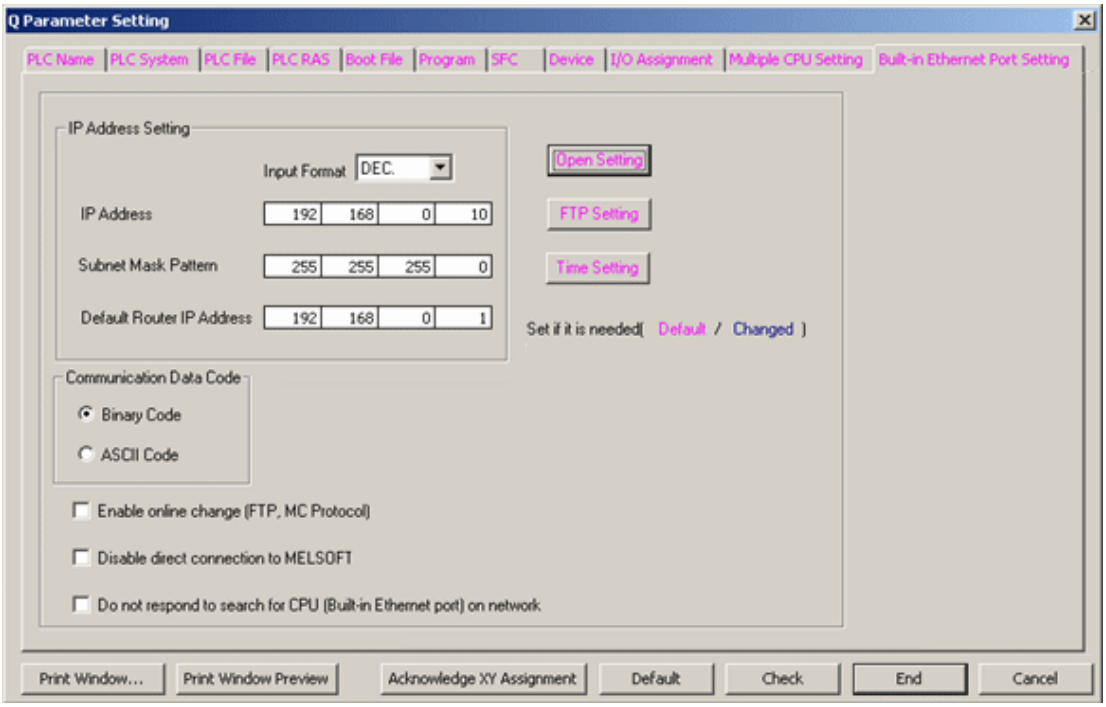
1. To start, create a new project for a Q Series (Q mode) PLC. Alternatively, open and edit an existing project.

2. Next, select **PLC Parameter**.



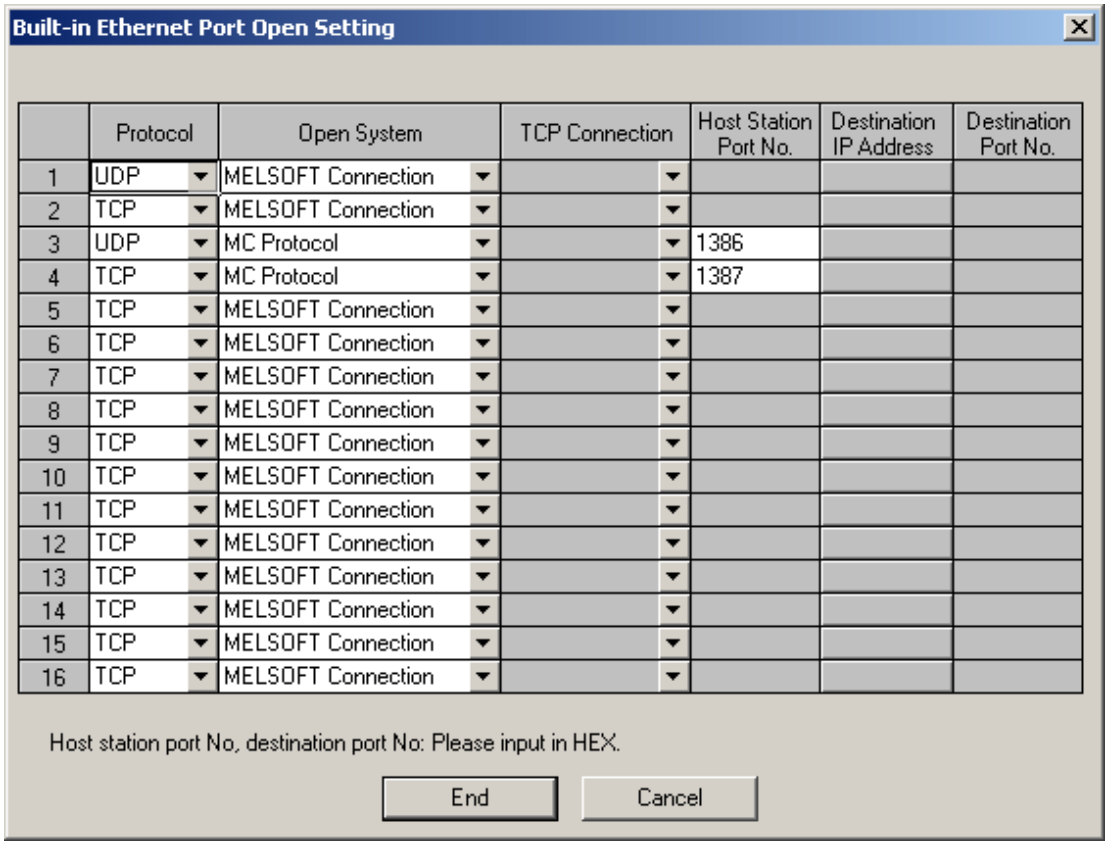
3. Open the **Built-in Ethernet Port Setting** tab, and then make the following changes:

   - Beneath **IP Address Setting**, fill in all required information.
   - Beneath **Communication Data Code**, select **Binary Code**.



4. Next, click **Open Setting**, and then make the following changes:

- Specify the **Protocol**. Options include **UDP** or **TCP**.
- Specify the **Open System** as **MC Protocol**.
- Specify the **Host Station Port No**.



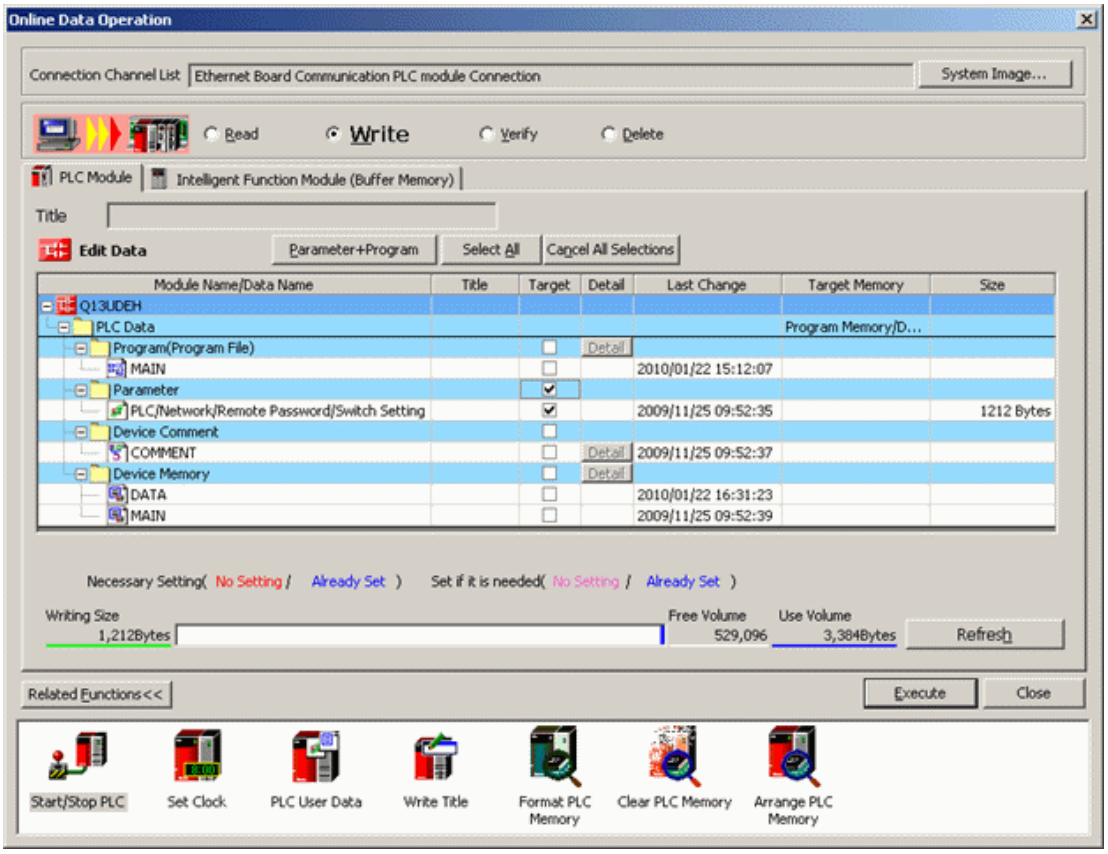
Note: In the example above, the local port numbers 4998 (1386H) and 4999 (1387H) are used.

Important: The driver's default port settings of 5000 UDP and 5001 TCP are not valid port numbers for the built-in Ethernet port. The driver uses decimal numbers for the port number; GX Works2 uses hexadecimal number for the port numbers. Valid port number setting ranges are 0401H (1025) to 1387H (4999), and 1392H (5010) to FFFEH (65534).

5. Click **End**.

Writing the Network Parameters to the PLC

After all network parameters have been specified, they must be written to the PLC. To do so, click **Online** | **Write To PLC...**. Then, check **Parameter** (located beneath **Target**) and then click **Execute**.



Note: Users must cycle the power on the PLC for the network parameter changes to take effect.

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